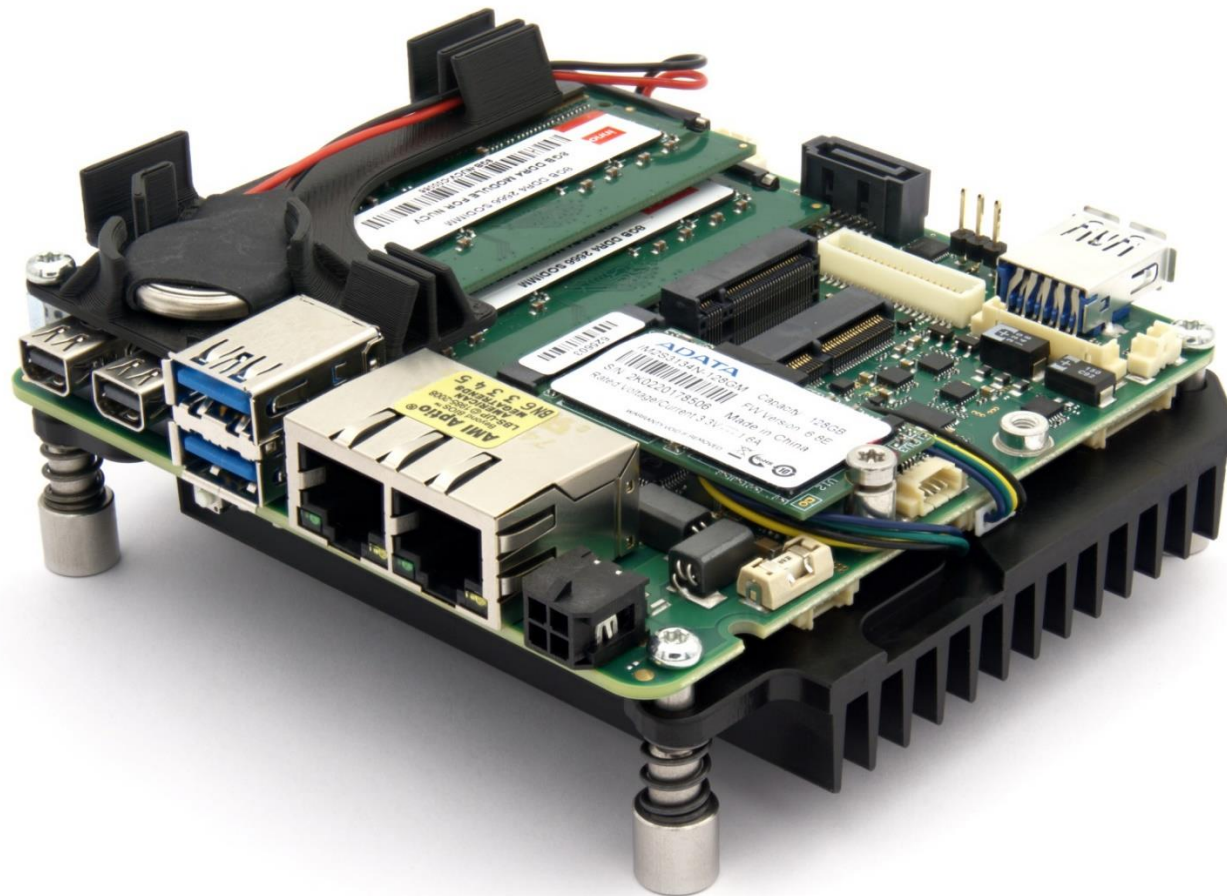


PROFIVE® NUCD „ready to use” 15 W Hardware Reference Manual – P – Rev. 5



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This product cannot be used as a stand-alone product!

Therefore, it has to be integrated together with other products like power supplies, mass storage devices (i. e. hard disks), etc. to be functional.

To meet FCC and/or CE requirements every component as well as the combination of all components has to be validated against all standards required for the end product.

In order to meet FCC and/or CE requirements this product has to be integrated into a proper housing, which provides appropriate shielding and insulation.

It is on the customer's sole responsibility to assure that this end-product meets all required standards.

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This product will be free from defects in workmanship and material under normal and proper use for the period of time defined in our General Terms of Business, effective the date of the original shipment from E.E.P.D. GmbH.

In the event of a warranty claim for defects, which appear within the warranty period, customer shall deliver the product along with proof of purchase to the original place of purchase, shipping prepaid. Repair, replacement or refund of the purchase price of the defective product will be at the sole option of the manufacturer. All transportation risks and costs in connection with warranty service are the responsibility of the customer.

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Product Returns

If you return the product to E.E.P.D. GmbH please remove all connections and peripheral equipment.

Protect the unit through a suitable packaging, preferably use the original packaging.

Packaging

The product is in a protective package to avoid damage during transport. This protective package should be environmentally friendly recycled after use.

Disposal of Device



At the end of the lifetime please dispose and/or recycle the components of the device accordingly.

Technical Support

For technical information about hardware and software please contact: support@eepd.de

Revision History

Date	Version	Changes	Proofed
01.04.2022	1.0	First release	
18.07.2023	1.1	BIOS Update	
25.02.2025	5.0	Update to HW Rev.5	

Revision History

Table of Contents

General Notes.....1
Revision History.....5
Used Symbols7
1 Product Description.....8
 1.1 Feature Overview.....8
 1.2 Environmental Specification.....10
2 System Overview11
3 Assembly.....12
 3.1 Product Views12
 3.2 Dimensions14
4 Feature Details.....16
 4.1 Connection Overview17
 4.2 Mini-DP++ Connectors.....19
 4.3 Dual-USB 3.2 Port.....20
 4.4 Rear USB 3.2 Port20
 4.5 2.5 Gigabit Ethernet Dual-Port21
 4.6 RS232 Connector.....21
 4.7 RS232/485 Connector.....22
 4.8 Power Connector23
 4.9 DDR4 SO-DIMM Sockets.....24
 4.10 Audio Connector25
 4.11 M.2 Sockets26
 4.12 SATA Data Connector.....27
 4.13 SATA Power Connector.....27
 4.14 LVDS Connector – OEM/ODM only28
 4.15 LC-Display Backlight Connector – OEM/ODM only.....29

4.16 Feature Connector 30
 4.17 Battery Connector 31
 4.18 Battery..... 31
 4.19 Power Button Connector 32
 4.20 Power Button, Power and SATA LEDs..... 32
 4.21 Reset Button Connector 33
 4.22 MicroSD Card Slot..... 33
 4.23 MicroSIM Card Slot 33
 4.24 FAN Connectors..... 34
 4.25 USB Internal Connector 35
5 BIOS**36**
 5.1 Entering Setup..... 36
 5.2 Most Common Settings 36
 5.3 Main Menu..... 37
 5.4 Advanced Menu 38
 5.5 Security Menu 45
 5.6 Power Menu 47
 5.7 Boot Menu..... 47
 5.8 AMD PBS Option..... 49
 5.9 AMD CBS Option 50
 5.10 Exit Menu 56
Index of Figures.....**57**
Index of Tables**59**
Terminology.....**60**
Classified Index**61**

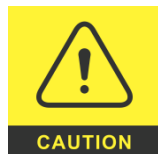
Used Symbols



The red danger sign warns you when a wrong or missing action dramatically endangers your life or health. The used components as well as the peripheral components could be destroyed.



The orange warning sign warns you when a wrong or missing action could seriously harm your health or destroy the used components.



The yellow caution sign warns you when a wrong or missing action could damage the component.



The yellow ESD sign draws your attention that static sensitive parts of the component could be destroyed. Unpack shielded components only with ESD protections like an ESD wrist strap.



The information sign gives you more information and advice for optimal use of this product. For example, it helps you to purchase necessary or optional accessories.

1 Product Description

In the first chapter you will get a glance of all the integrated features. For further information and a brief description of all possible changes and extensions please refer to the corresponding chapter.

1.1 Feature Overview

Power Supply

Designed for min. 8 V to max. 32 V (DC) single power supply, max. 7.5 A, automotive grade | KL15 (ignition), min. 90 W, 8 A fused.



Current rating of the power supply has to exceed the expected current draw depending on the system configuration, CPU variant and its TDP settings, and peripheral devices used with the system. Power supplies might derate due to lower efficiency at higher operating temperature. Insufficient power supply might lead to the system instability, shutdown and data loss.

Power Connector

Four-pin power connector with ignition input.

Molex 43045 Series or compatible 3.00 mm Micro MF connector with lock, max. 8.5 A.

Power Limit

Internal and external 3.3 V power max. 20 W.

Internal and external 5 V power max. 30 W.

Internal and external power is related to m.2 connectors, USB, FAN, SATA power and display.

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Processor Support

AMD Ryzen™ Embedded R2000 processor series:

R2312 / 2C / 4T / 2.7 GHz – 3.5 GHz / 15 W (12 – 25 W)

R2314 / 4C / 4T / 2.1 GHz – 3.5 GHz / 15 W (12 – 35 W)

Ethernet Controller

2x Intel® i225-LM 2.5 Gbit/s Ethernet PCI Express® controllers with IEEE1588, TSN-support, Wake-on-LAN supported by ETH 1.

RAM Support

Max. 2x 32 GB dual channel up to DDR4-3200 SO-DIMM memory with ECC support.

Display

2x Mini-DP++ connectors (v1.4), up to 3840 x 2160 @ 60 Hz.

1x LVDS up to 3840 x 2160 @ 60 Hz (OEM/ODM only).

USB Support

1x Dual-USB 3.2 Gen2 (10 Gb/s, OCP = 1.5 A each), Type A at the front side.

1x Single-USB 3.2 Gen2 (10 Gb/s, OCP = 1.5 A), Type A at the rear side.

1x USB 3.2 Gen1 (5 Gb/s, OCP = 900 mA) on Molex (PicoBlade) connector (not available with R2312)

Up to 2x USB 2.0 (480 Mb/s, OCP = 500 mA each) on Molex (PicoBlade) connector (OEM/ODM only)

Serial Ports

1x RS-232/485 (FDX|HDX) port on internal Molex (PicoBlade) connector.

1x RS-232 port on internal Molex (PicoBlade) connector.

Audio

HDA Codec; MIC IN, stereo HP OUT, stereo Line IN, stereo Line OUT on internal Molex (PicoBlade) connector.

Storage Support

1x microSD XC card slot at the rear side, supports A1 Application Performance Class, UHS-Class1.

1x M.2 Key M, 2242, PCIe Gen3 x1 NVMe or SATA SSD.

1x SATA (6G) upright connector with separate power Molex (PicoBlade) connector (+5V DC, max. 1.5 A, not fused).

Expansion Slots

1x M.2 Key E, 2230, support for PCIe Gen3 x1 / USB 2.0, e. g. for WLAN/BT.

1x M.2 Key B, 2242/3042, support for PCIe Gen3 x1 / USB 2.0, e. g. for WWAN, μ SIM card available on board, see chapter 4.23 for details.

Cooling

CPU fan, 5 V DC, max. 250 mA, not fused

Optional auxiliary fan, 5 V DC, max. 250 mA, not fused

OS Support

Microsoft® Windows® 11

Microsoft® Windows® 11 IoT Enterprise

Microsoft® Windows® 10

Microsoft® Windows® 10 IoT Enterprise

Linux Ubuntu 24.04 LTS

Extended Features

Onboard μ -Controller-IC for:

Input voltage level detection

Power-up sequencing and timing

System reset management

Fan control

Temperature monitoring

Watchdog

Power and reset button input

Feature connector (max. 4x GPIO [3.3 V], max. 3x GPO [3.3 V/50 kHz], Status LED)

Power LED onboard

Status LED onboard

LVDS panel backlight

RTC battery connector

Onboard Trusted Platform Module

TPM 2.0 support (Infineon SLB 9670) (R2312 variants OEM only)

Size approx. 107 x 113 x 46 mm

Weight approx. 330g + options

1.2 Environmental Specification

Max. Operating Temperature

0 °C to +60 °C ambient commercial grade, when adequate heatsink/cooling is provided.

CPU throttling may occur at higher ambient temperatures.

Max. Storage Temperature

-40 °C to +85 °C, non-condensing

Max. rel. Humidity for all versions

95 % @ 40 °C non-condensing while stored, 89% while working

2 System Overview

This chapter describes the main hardware components of the NUCDX board.

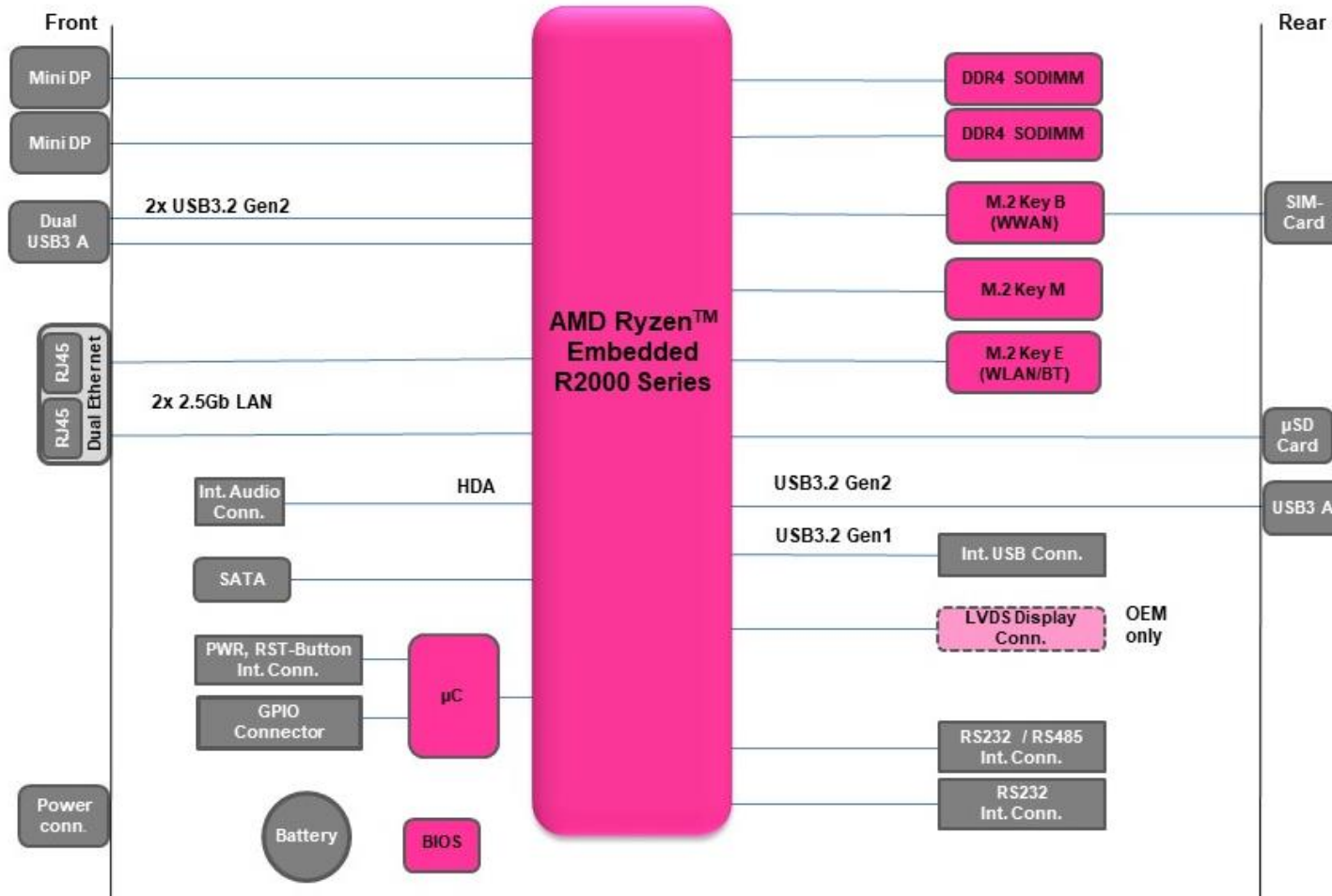


Fig. 1: System overview

3 Assembly



The NUCD “ready to use” is designed to be integrated and used as it is. Do not disassemble the cooling solution, this will void the warranty.
The illustration shows an example of a variant with active cooling.

3.1 Product Views

Top View

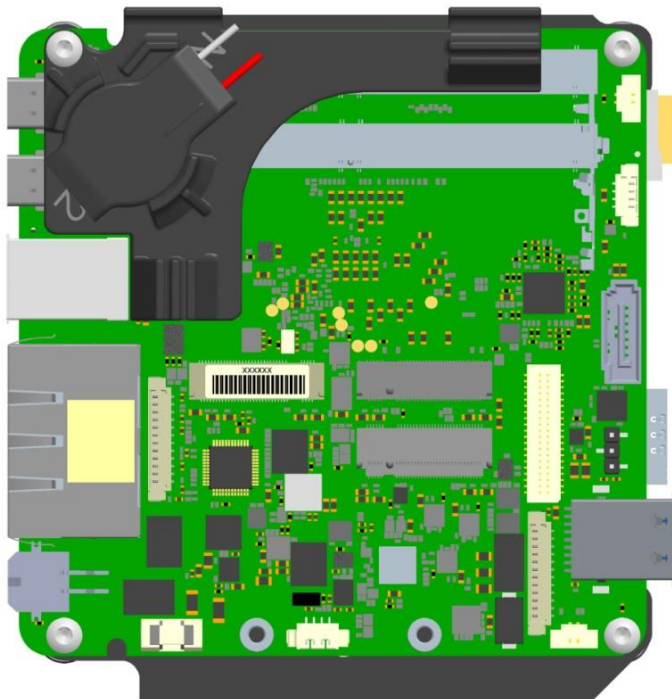


Fig. 2: Top view

Bottom View

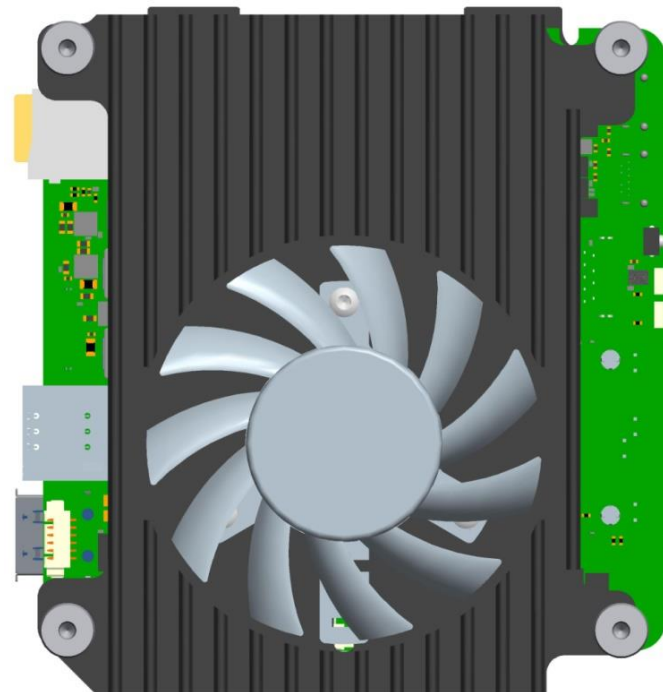


Fig. 3: Bottom view with active cooling

Front View



Fig. 4: Front view

Rear View

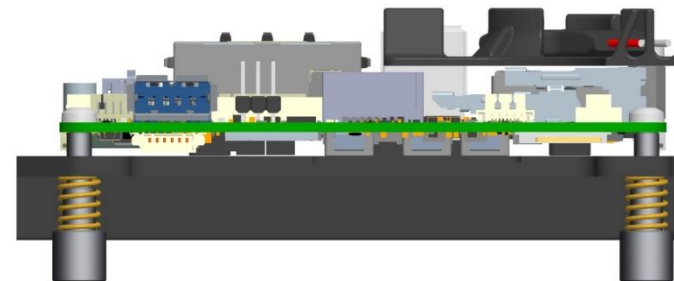


Fig. 6: Rear view

Assembly

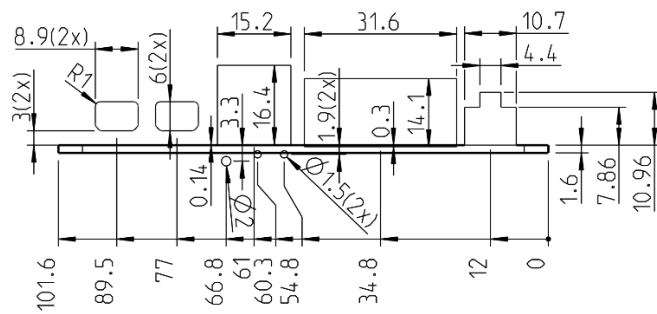


Fig. 5: recommended cutouts front panel, values [mm]

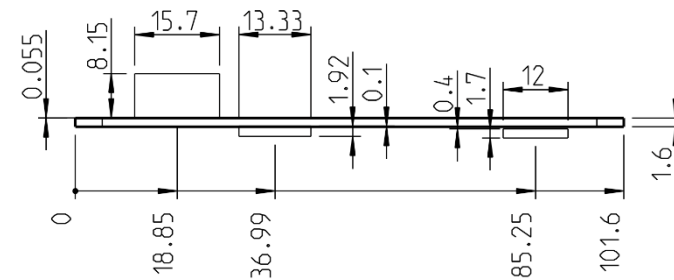


Fig. 7: recommended cutouts rear panel, values [mm]

Side View

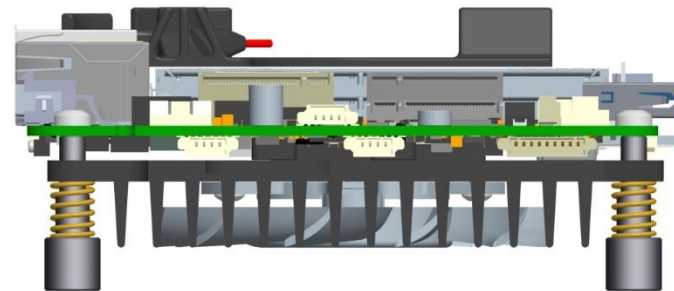


Fig. 8: Side view

3.2 Dimensions

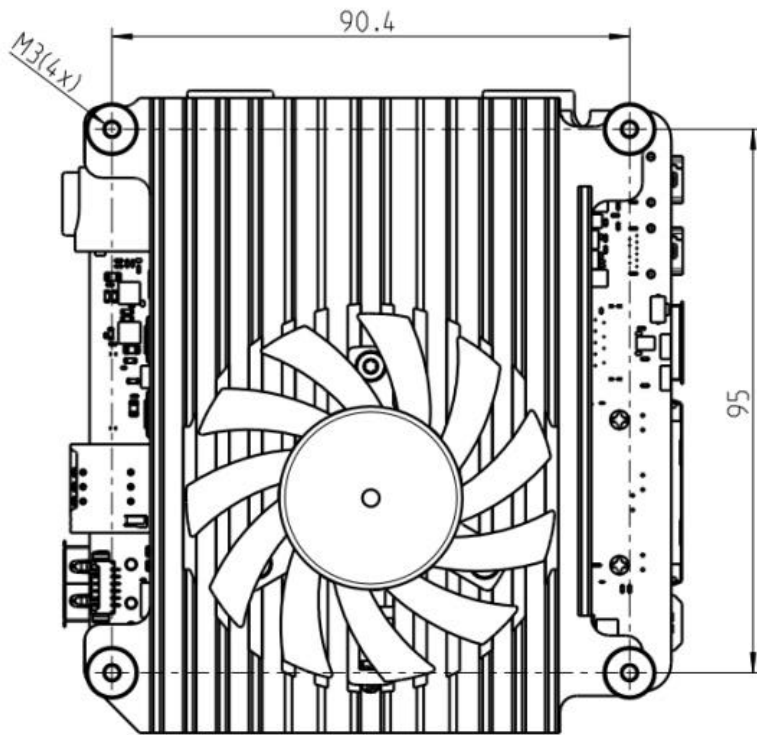


Fig. 9: Dimensions bottom side, values [mm]

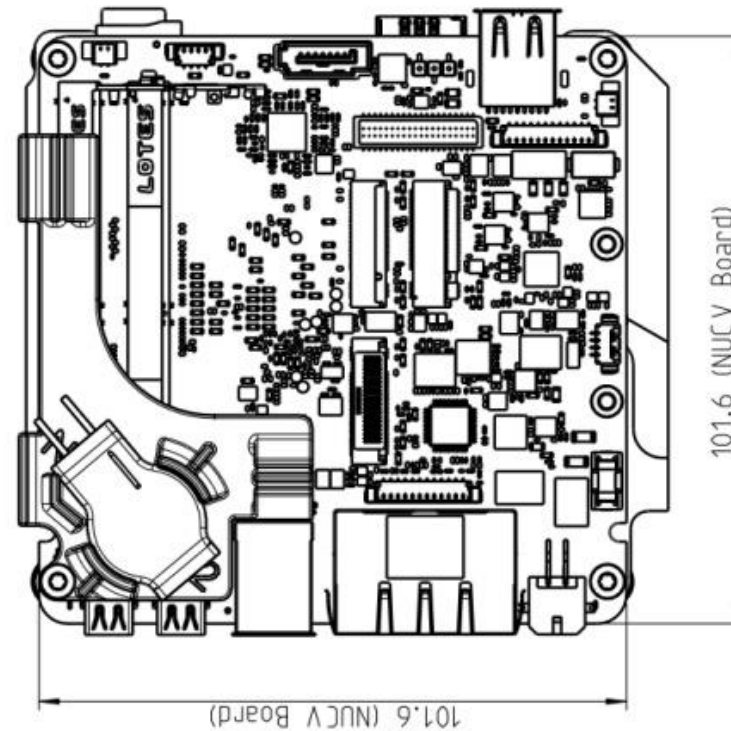


Fig. 10: Dimensions top side, values [mm]

Assembly

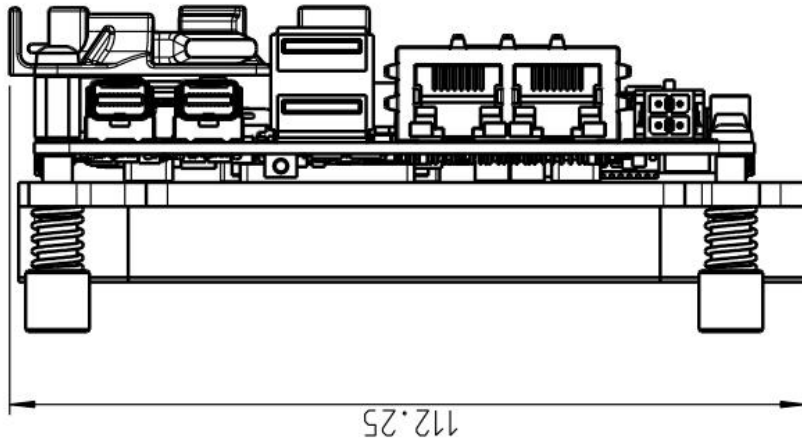


Fig. 11: Dimensions front side, values [mm]

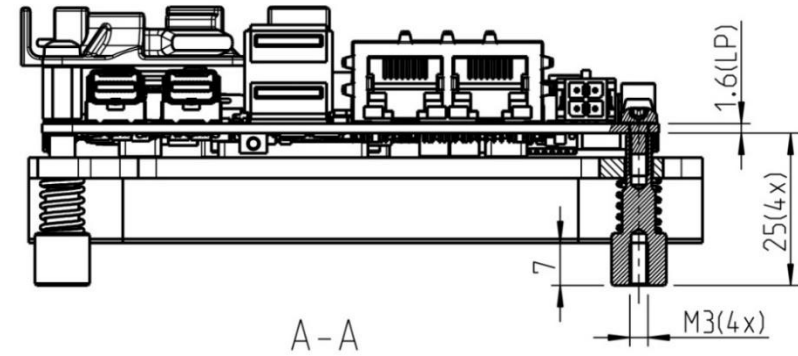


Fig. 13: Dimensions threaded bolts, values [mm]

Assembly

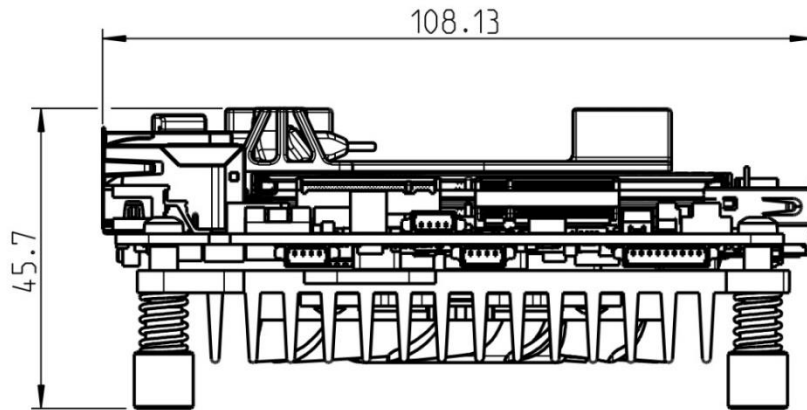


Fig. 12: Dimensions side, values [mm]

4 Feature Details

The following schedule contains all the necessary information to connect the board to your peripheral equipment.

On the left side of the page, you will see the position on the board (red mark) and on the right side a magnified image of the explained item.

The explanation provides general information, electrical specifications and a pin assignment table.

All voltages are DC:	
VCC	5 V +/- 5%
P3V3	3.3 V +/- 5%
P12V_DISPLAY	12 V +/- 5%
PVIN (min. – max.)	8 – 32 V
VDD_LCD	3.3 V/5 V +/- 5%

4.1 Connection Overview

Top Side

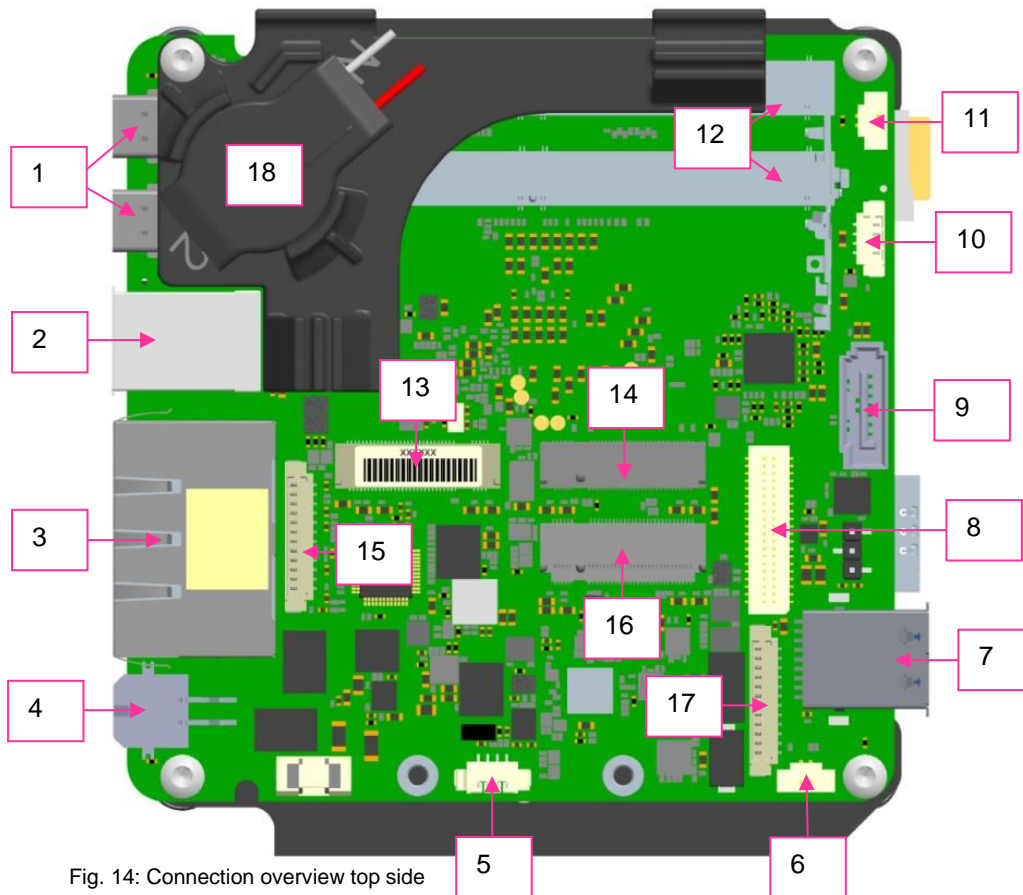
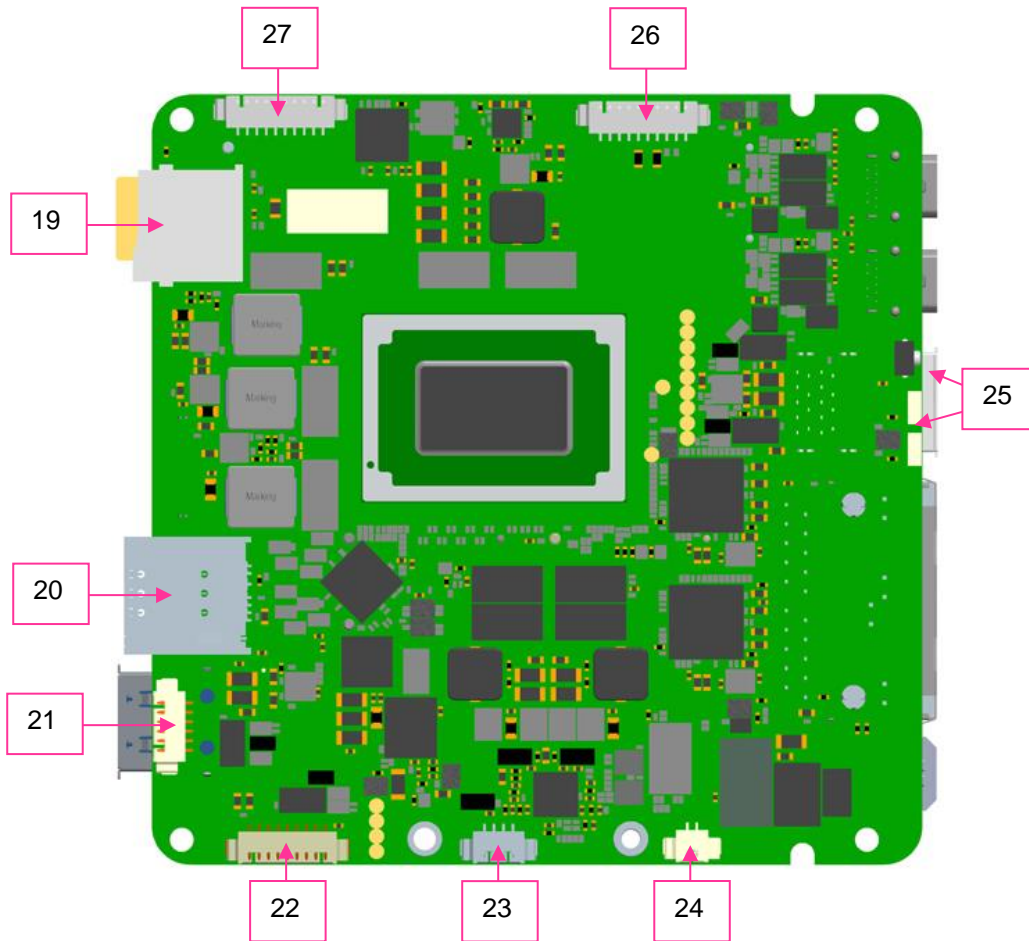


Fig. 14: Connection overview top side

- 1 – Mini-DP++ connectors, see chapter 4.2
- 2 – Dual-USB 3.2 port, see chapter 4.3
- 3 – 2.5 Gigabit Ethernet Dual-Port, see chapter 4.4
- 4 – Power connector, see chapter 4.6
- 5 – Auxiliary fan connector, see chapter 4.22
- 6 – Reset button connector, see chapter 4.21
- 7 – Rear USB 3.2 port, see chapter 4.4
- 8 – LVDS connector, see chapter 4.3
- 9 – SATA data connector, see chapter 4.12
- 10 – SATA power connector, see chapter 4.13
- 11 – Battery connector, see chapter 4.7
- 12 – DDR4 SO-DIMM sockets, see chapter 4.9
- 13 – M.2 Key M socket, see chapter 4.11
- 14 – M.2 Key B socket, see chapter 4.11
- 15 – Audio connector, see chapter 4.10
- 16 – M.2 Key E socket, see chapter 4.11
- 17 – LC-Display backlight connector, see chapter 4.15
- 18 – Battery, see chapter 4.18

Bottom Side



- 19 – MicroSD card slot, see chapter 4.22
- 20 – MicroSIM card slot, see chapter 4.23
- 21 – RS232 connector, see chapter 4.6
- 22 – internal USB connector, see chapter 4.25
- 23 – CPU fan connector, see chapter 4.22
- 24 – Power button connector, see chapter 4.17
- 25 – Power Button, Power and SATA LEDs, see chapter 4.20
- 26 – Feature connector, see chapter 4.16
- 27 – RS232/485 connector, see chapter 4.7

Fig. 15: Connection overview bottom side

4.2 Mini-DP++ Connectors

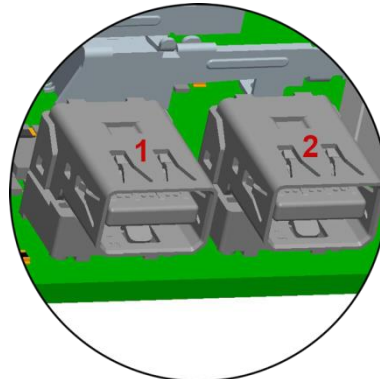
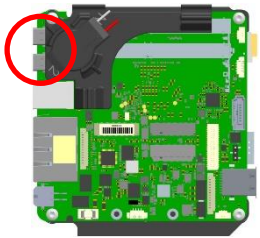


Fig. 16: Mini-DP++ connector detail

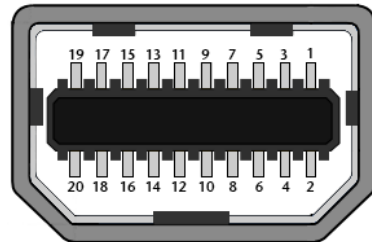


Fig. 17: Mini-DP++ connector schematic



Important Note:

There are two types of DisplayPort cables:

If you connect the display **directly** to the Mini DP, please use cable with **Pin 20 not connected**.

If you use **active cables/adapters** (e. g. Mini DP to DP, Mini DP to HDMI), please use cable with **Pin 20** (supply voltage) on both ends that are **connected**.

Possible effects if wrong cable is used:

System might not start up properly.

Dongle does not work properly (black display).

4.3 Dual-USB 3.2 Port



USB 3.2 ports providing max. 1.5 A per port (OCP).
Support for USB 2.0 and USB 3.2 Gen2 (10Gb/s).

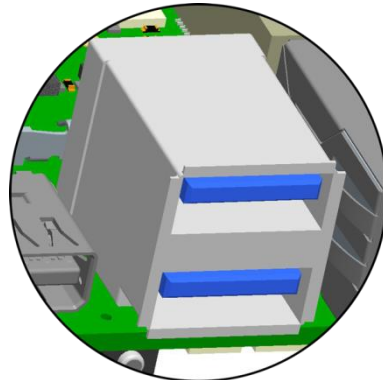
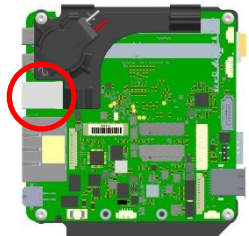


Fig. 18: Dual-USB 3.2 port detail

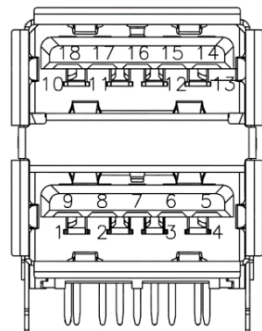


Fig. 19: Dual-USB 3.2 port schematic

4.4 Rear USB 3.2 Port



USB 3.2 ports providing max. 1.5 A per port (OCP).
Support for USB 2.0 and USB 3.2 Gen2 (10Gb/s).

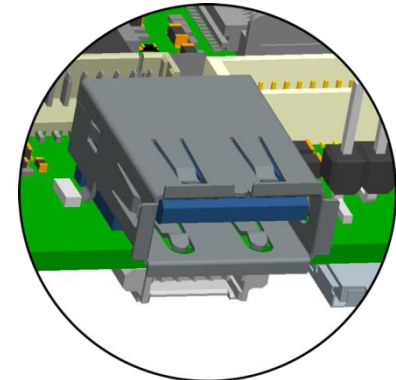
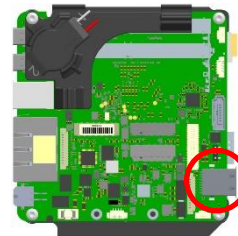


Fig. 20: USB 3.2 port detail

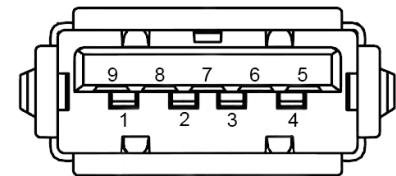


Fig. 21: USB 3.2 port schematic

4.5 2.5 Gigabit Ethernet Dual-Port

The Dual-Ethernet socket uses two Intel® i225 controllers. ETH1 supports Wake-on-LAN.

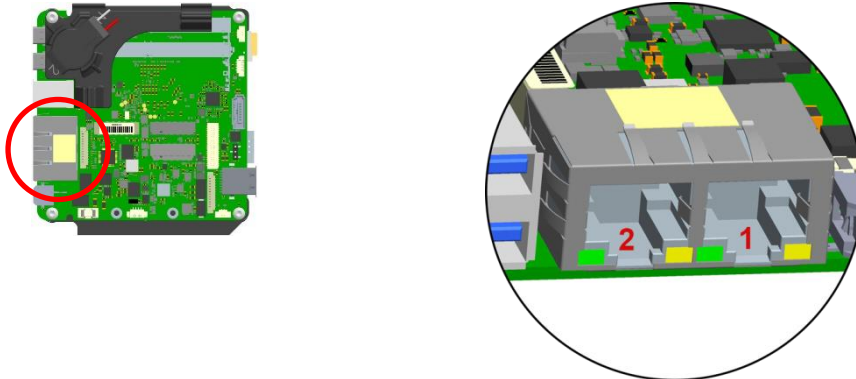


Fig. 22: Ethernet dual-port detail

Yellow LED

Speed-LED is on during 2.5 or 1 Gbit transmission and switched off during 10/100 Mbit transmission.

Green LED

Link-/Activity-LED is permanently on to indicate an active connection on the Ethernet port. LED blinks during communication with the Ethernet network.

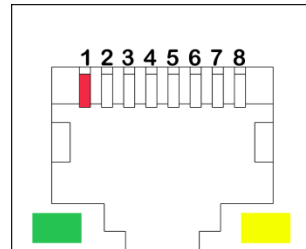


Fig. 23: Ethernet port schematic

4.6 RS232 Connector

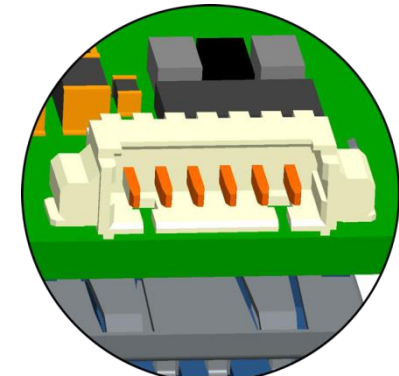
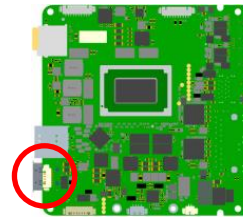


Fig. 24: Molex RS232 connector detail



Counterpart – plug:
MOLEX Pico Blade
0510210600

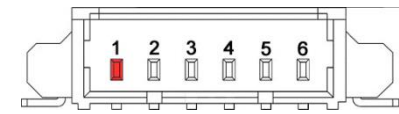


Fig. 25: Molex RS232 connector schematic

Pin	Signal
1	RXD1
2	RTS1_m
3	TXD1
4	CTS1_m
5	VCC 5 V +/-5%, max. 500 mA not fused
6	GND

Tab. 1: RS232 connector

4.7 RS232/485 Connector

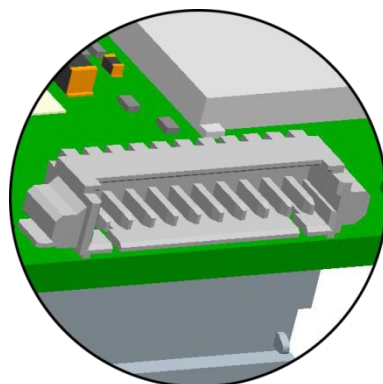
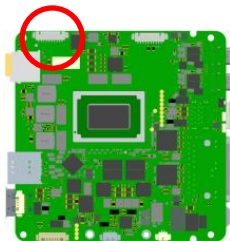


Fig. 26: Molex RS232/485 connector detail



Counterpart – plug:
MOLEX Pico Blade
0510211000



Fig. 27: Molex RS232/485 connector schematic

Pin	Signal RS232*	Signal RS485/FDX*	Signal RS485/HDX for Windows 10/11 only*
1	-	RS485-TX_N	RS485-RX/TX_N
2	-	-	
3	RXD0	RS485-TX_P	RS485-RX/TX_P
4	RTS0_m	-	
5	TXD0	RS485-RX_P	
6	CTS0_m	-	
7	-	RS485-RX_N	
8	-	-	
9	VCC 5 V +/-5%, max. 500 mA not fused	VCC 5 V +/-5%, max. 500 mA not fused	VCC 5 V +/-5%, max. 500 mA not fused
10	GND	GND	GND

Tab. 2: RS232/485 connector

*See BIOS setting chapter 5.9.3.2

Feature Details

4.8 Power Connector



For a stable operation:

Please ensure a system power supply of min. 90 W. Power source must be capable to respond to fast load changes!
Supply voltage (V_{in}) min. 8 V / max. 32 V.

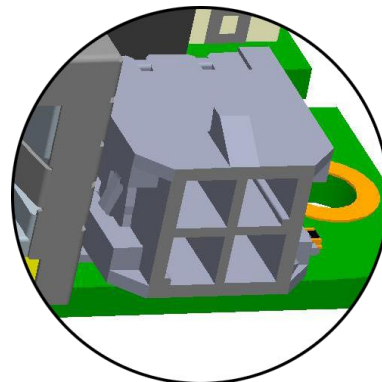
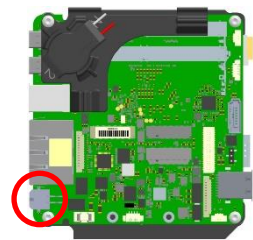


Fig. 28: Power connector detail

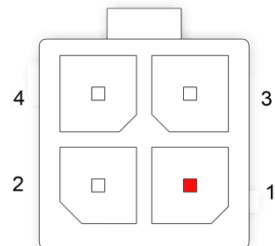


Fig. 29: Power connector schematic



Counterpart – plug:

Molex 43045 Series or compatible 3.00 mm Micro MF connector with lock, max. 8.5 A

Pin	Signal
1	GND_IN, max. 8.5 A
2	GND_IN, max. 8.5 A
3	PVIN, max. 8.5 A
4	KL_15*, max. 8.5 A

Tab. 3: Power connector

*KL_15 is ignition enable pin required for automotive applications.

KL_15 is default off in BIOS setup (see chapter 5.4.8).

When enabled:

- Switching on the PC with ignition
- Switching off when the ignition is off with adjustable timers (timer 1 – shutdown; timer 2 – hard off, if shutdown times out; Countdown timer between 1 and 65535 seconds)

The timers can be set via the “ignition signal” BIOS menu. Default is 180 seconds: 180 seconds after the ignition is off, the board is shut down by the onboard microcontroller.

4.9 DDR4 SO-DIMM Sockets

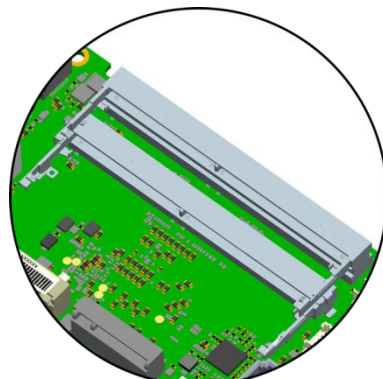


Fig. 30: DDR4 SO-DIMM socket detail



Use only 1.2 V DDR4 SO-DIMM modules compliant with the DDR4 Standard.
Dual channel DDR4 SO-DIMM memory, max. 2x 32 GB, up to 3200 MT/s, with ECC support.

Assembly:

First slide RAM module into the RAM socket.
Then press the module in direction to the board till you hear it snap.

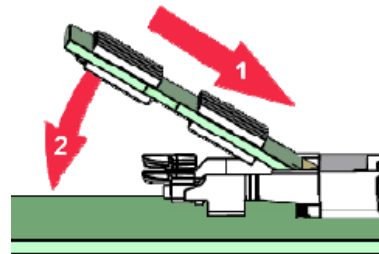


Fig. 31: RAM assembly

Disassembly:

First press both clamps outwards.
Then the RAM module will set upright automatically.
Remove the module from the socket.

4.10 Audio Connector

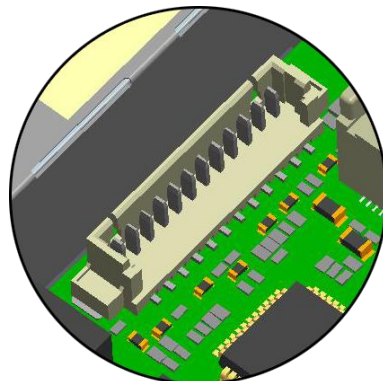
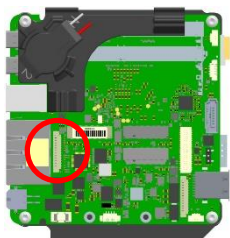


Fig. 32: Molex audio connector detail



Counterpart – plug:
MOLEX Pico Blade
0510211200



Fig. 33: Molex audio connector schematic



For optimum audio performance:
Please ensure to use cables provided by E.E.P.D.

Pin	Signal
1	JD_LINE_IN*
2	LINE_IN_L
3	LINE_IN_R
4	GND_AUDIO
5	MIC_IN
6	GND_AUDIO
7	JD_LINE_OUT*
8	LINE_OUT_L
9	LINE_OUT_R
10	GND_AUDIO
11	HP_OUT_L
12	HP_OUT_R

Tab. 4: Audio connector

*JD = Jack Detect

4.11 M.2 Sockets

M.2 Socket Key M

2242, PCIe Gen3 x1 NVMe or SATA SSD, P3V3 max. 3.5 A, not fused

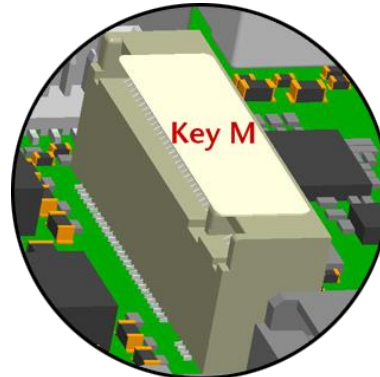


Fig. 34: M.2 Key M connector detail

M.2 Socket Key E

2230, support for PCIe Gen3 x1 / USB 2.0, P3V3 max. 2 A, not fused

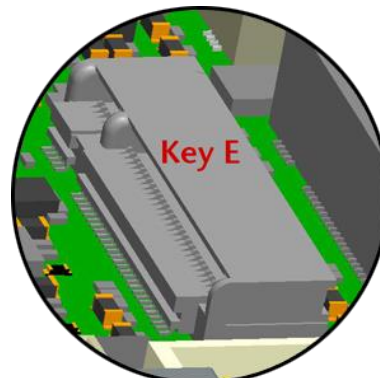


Fig. 35: M.2 Key E connector detail

M.2 Socket Key B

2242/3042, support for PCIe Gen3 x1 / USB 2.0, P3V3 max. 2.5 A, not fused

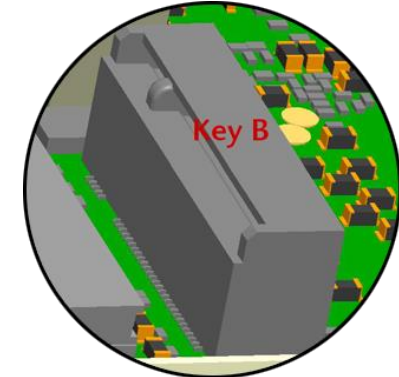
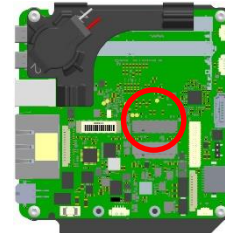


Fig. 36: M.2 Key B connector detail

Installing M.2 modules

Insert the M.2 module into the corresponding slot at an angle. Press it down on the side that protrudes and secure it with the screw provided.

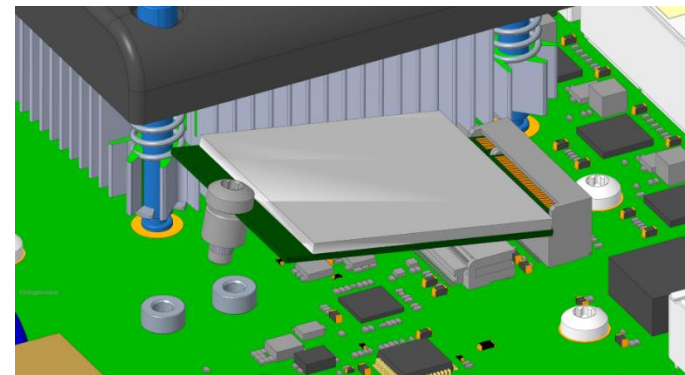


Fig. 37: M.2 Key B module assembly

4.12 SATA Data Connector

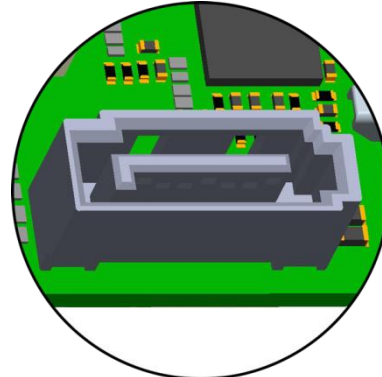
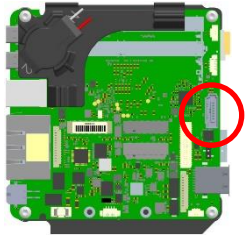


Fig. 38: SATA data connector detail

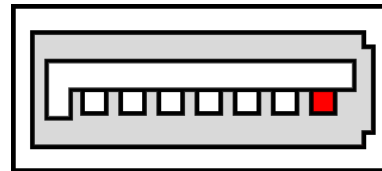


Fig. 39: SATA data connector schematic

4.13 SATA Power Connector

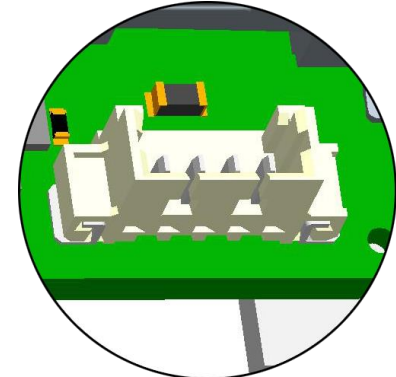
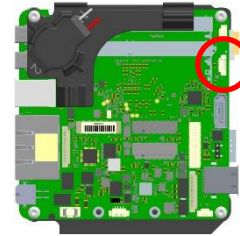


Fig. 40: Molex SATA power connector detail



Counterpart – plug:
MOLEX Pico Blade
0510210400

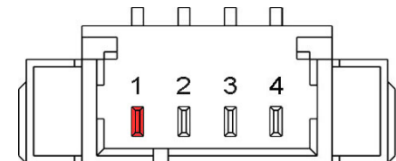


Fig. 41: Molex SATA power connector schematic

Pin	Signal	
1	VCC 5V +/- 5%	max. 1.5 A, not fused
2	VCC 5V +/- 5%	
3	GND	
4	GND	

Tab. 5: SATA power connector

4.14 LVDS Connector – OEM/ODM only

Interface for controlling TFT- and LC-Displays. For specific panel support, please contact E.E.P.D.



The connector supplies the LVDS data/logic section of your LC-display with 3.3 V or 5 V.

Selection via jumper.

Please assure the correct setting of the LVDS panel power supply before connecting the LC-display to the board.

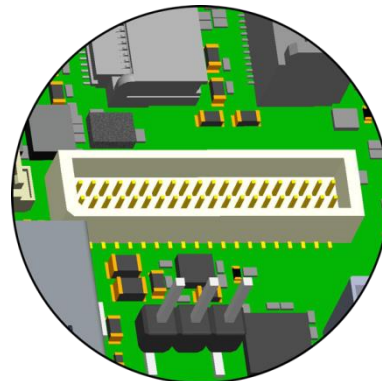
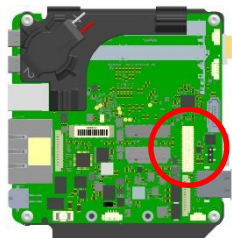


Fig. 42: LVDS data connector detail



Counterpart – plug:
JST SHDR-40V-S-B

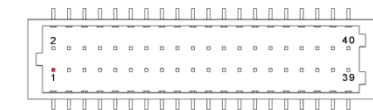


Fig. 43: LVDS dataconnector schematic

4.14.1 Jumpersetting for LVDS Data section of the LC-display – OEM/ODM only

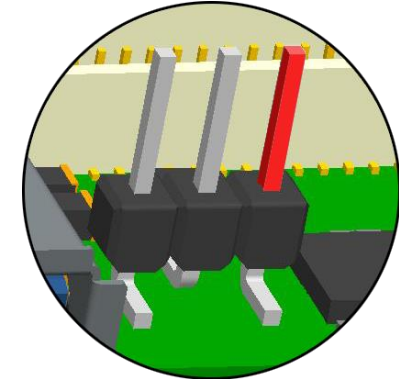
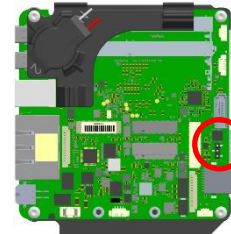


Fig. 44: STL3X1 detail

Pin	Signal
1	VCC → 5V +/- 5%
2	VDD_LCD*, not fused
3	P3V3 → 3.3 V +/- 5%

Tab. 6: STL3X1

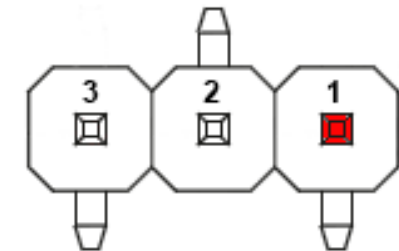


Fig. 45: STL3X1 schematic

*VDD_LCD = 3.3 V → set Jumper between Pin 2 and 3
VDD_LCD = 5 V → set Jumper between Pin 1 and 2

Maximum current draw from VDD_LCD line is 3.5 A (not fused). Please be aware of the total power limit for the external and internal devices on 3.3 V and 5 V lines as specified in Chapter 1.1.

4.15 LC-Display Backlight Connector – OEM/ODM only

Connector supplies the power section of your LC-Display with power.



The connector supplies the Backlight Inverter with 5 V and 12 V (maximum load of 3 A).

For 12 V backlight supply PVIN must be min. 15 V.

Please pay attention to this, when selecting the Backlight Inverter.

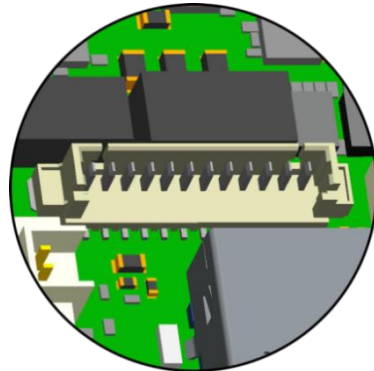
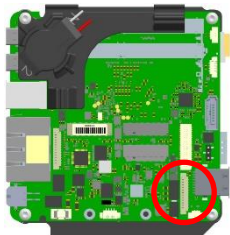


Fig. 46: Molex LC-display backlight connector detail



Counterpart – plug:
MOLEX Pico Blade
0510211200

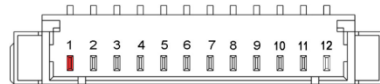


Fig. 47: Molex LC-display backlight connector detail

Pin	Signal	
1	VCC 5V +/- 5%	max. 3 A, not fused
2	VCC 5V +/- 5%	
3	VCC 5V +/- 5%	
4	P12V_DISPLAY 12V +/- 5%	max. 2.5 A, not fused
5	P12V_DISPLAY 12V +/- 5%	
6	P12V_DISPLAY 12V +/- 5%	
7	GND	
8	GND	
9	LCD_BKLEN	
10	GND	
11	BRT_ADJ (PWM)	
12	GND	

Tab. 7: LC-display backlight connector

4.16 Feature Connector

Status LED and GPIO Connector.

This feature requires Linux driver or Windows Library for OEM customers on request.

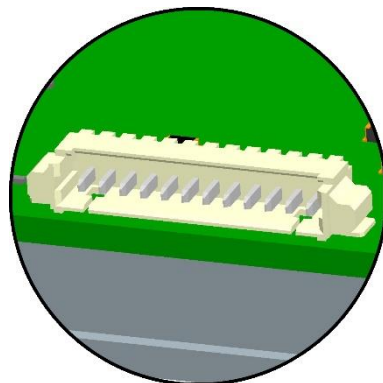
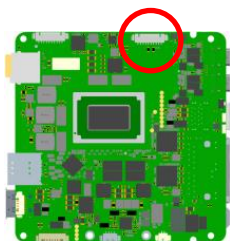


Fig. 48: Molex feature connector detail



Counterpart – plug:
MOLEX Pico Blade
0510211200

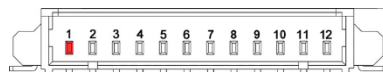


Fig. 49: Molex feature connector schematic

Pin	Signal
1	3.3 V +/- 5% 200 mA max., not fused
2	Reserved
3	GPIO0
4	GPIO1
5	GPIO2
6	GPIO3
7	GPO1
8	GPO2
9	GPO3
10	STATUS_LED_ANODE 3.3 V +/- 5% with 100 Ω series resistor (1/16W) 33 mA max., not fused
11	STATUS_LED_CATHODE
12	GND

Tab. 8: Feature connector

4.17 Battery Connector

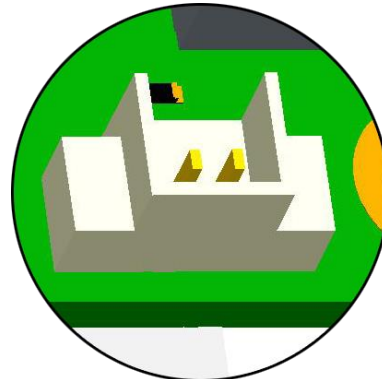
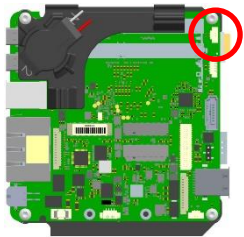


Fig. 50: Molex battery connector detail



Counterpart – plug:
MOLEX Pico Blade
0510210200

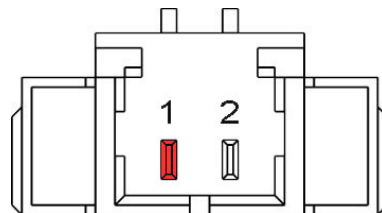


Fig. 51: Molex battery connector schematic

Pin	Signal
1	BATT
2	GND

Tab. 9: Battery connector

4.18 Battery

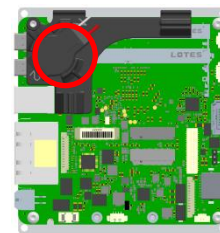


Fig. 52: Battery detail



Danger of explosion if battery is incorrectly replaced.

Use only Lithium cell assemblies from EEPD. Battery assemblies can be obtained through sales@eepd.de
For proper function of the board change the battery every five years.



Battery with cable:
Ordering number: W-BATCR1A

Feature Details

4.19 Power Button Connector

Press power button once to start up.
 Press power button to shut down into S3/S4/S5 (Standby/Hibernate/OFF; depending on OS setting).
 Hold power button (> 4 sec.) for hard shut down into S5 (OFF).

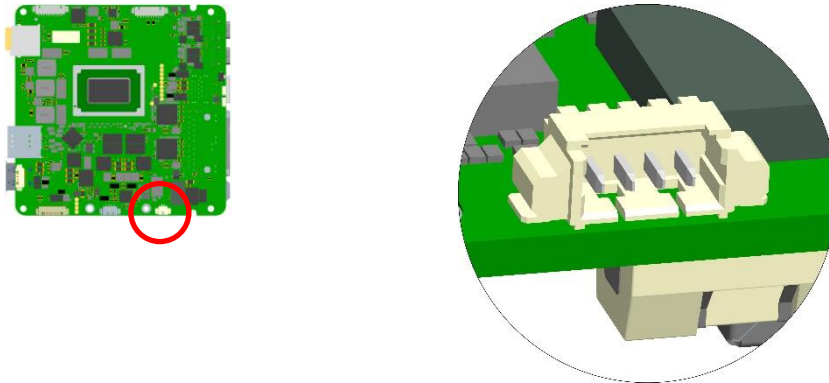


Fig. 53: Molex power button connector detail



Counterpart – plug:
 MOLEX Pico Blade
 0510210400

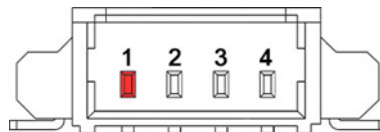


Fig. 54: Molex power button connector schematic

Pin	Signal	Pin	Signal
1	PWR_LED_ANODE 3.3V +/- 5% with 100 Ω series resistor, max. 33 mA, not fused	3	GND
2	PWR_LED_CATHODE	4	POWERBUTTON

Tab. 10: Power button connector

4.20 Power Button, Power and SATA LEDs

Press power button once to start up.
 Press power button to shut down into S3/S4/S5 (Standby/Hibernate/OFF; depending on OS setting).
 Hold power button (> 4 sec.) for hard shut down into S5 (OFF).

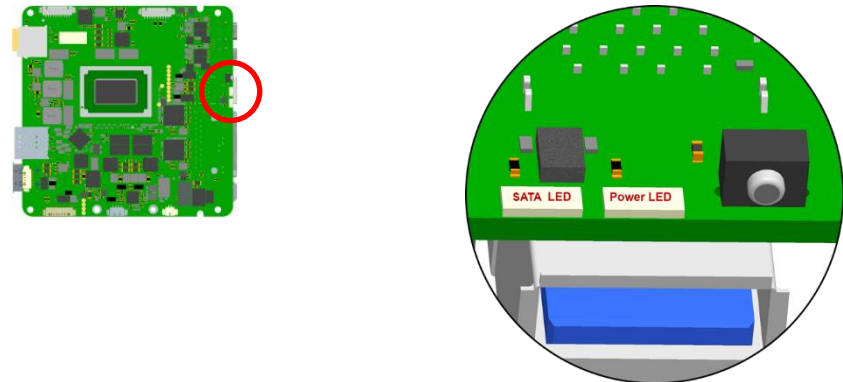


Fig. 55: Power button/power and SATA LEDs detail

4.21 Reset Button Connector

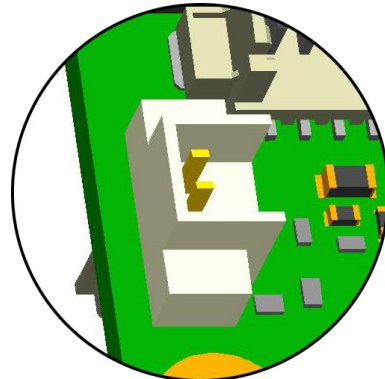


Fig. 56: Molex reset button connector detail



Counterpart – plug:
MOLEX Pico Blade
0510210200

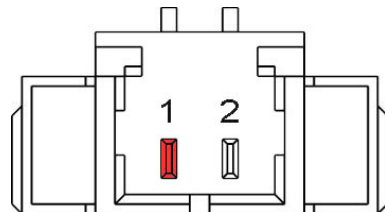


Fig. 57: Molex reset button connector schematic

Pin	Signal
1	GND
2	RESET_IN_m

Tab. 11: Reset button connector

4.22 MicroSD Card Slot

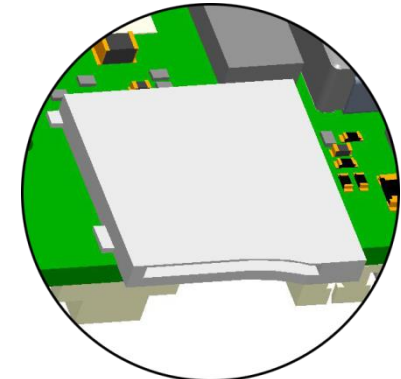
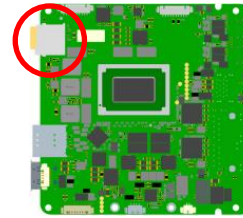


Fig. 58: MicroSD card slot detail

4.23 MicroSIM Card Slot

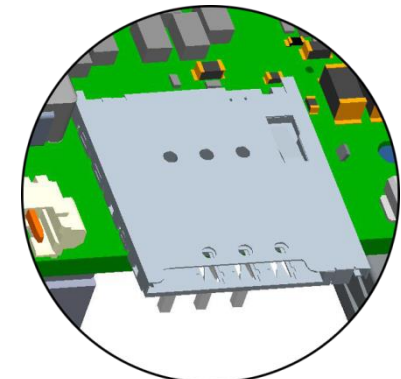
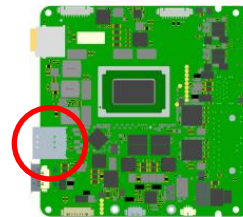


Fig. 59: MicroSIM card slot detail

4.24 FAN Connectors

CPU FAN Connector

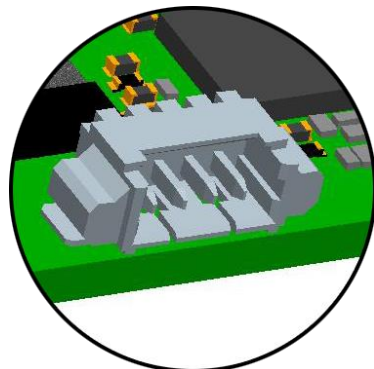
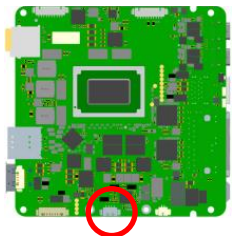


Fig. 60: Molex CPU fan connector detail

Auxiliary FAN Connector (e. g. for SSD)

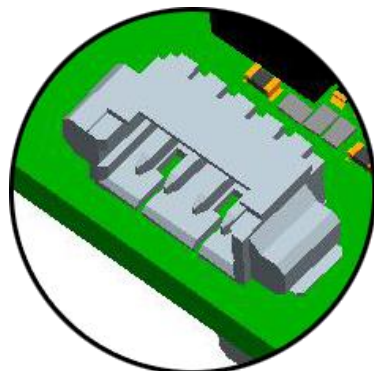


Fig. 61: Molex auxiliary fan connector detail



Counterpart – plug:
MOLEX Pico Blade
0510210400

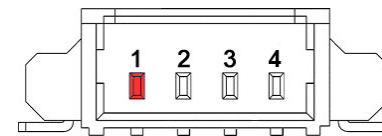


Fig. 62: Molex fan connectors schematic

Pin	Signal
1	GND
2	VCC 5V +/- 5%, max. 250 mA, not fused
3	FAN_TACHO
4	FAN_PWM

Tab. 12: Fan connectors



An incorrectly connected fan can cause damage to the fan and the board!

4.25 USB Internal Connector



USB 3.2 port providing max. 900 mA (OCP).
Support for USB 2.0 and USB 3.2 Gen1 (5 Gb/s).

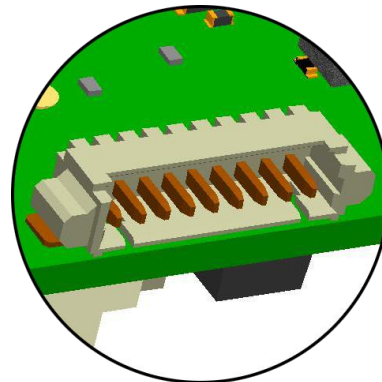
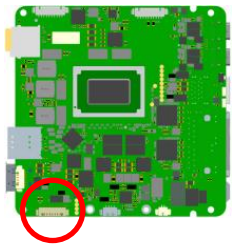


Fig. 63: Molex USB internal connector detail



Counterpart – plug:
MOLEX Pico Blade
0510210900

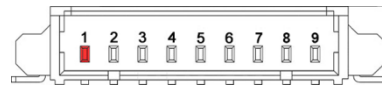


Fig. 64: Molex USB internal connector schematic

Pin	Signal
1	VCC_USBSS_4 → 5V +/- 5%, OCP = 900 mA
2	USB2_4_N
3	USB2_4_P
4	GND
5	USBSS_4_TX_N
6	USBSS_4_TX_P
7	GND
8	USBSS_4_RX_N
9	USBSS_4_RX_P

Tab. 13: USB internal connector

5 BIOS

The following description shows a snapshot of the BIOS setup. Later BIOS updates may change the content slightly.

Asterisk (*) indicates default setting.

5.1 Entering Setup

Power on the board and press and hold [ESC] immediately to enter Setup.

5.2 Most Common Settings

- Firmware / BIOS Version:
Setup Utility → Main (chapter 5.3)
or Setup Utility → AMD PBS → AMD Firmware Version (chapter 5.8.1)
- Boot / PXE Boot
Boot Manager
or Setup Utility → Boot (chapter 5.7)
- Change shared graphics memory
Setup Utility → AMD CBS → NBIO Common Options → GFX Configurations → UMA Frame Buffer Size (chapter 5.9.2.1)
- TDP, fan control, boost mode
TDP setting (chapter 5.9.2):
Setup Utility → AMD CBS → NBIO Common Options → System Configuration
Fan control (chapter 5.9.2.2):
Setup Utility → AMD CBS → NBIO Common Options → CPU and Auxiliary Fan Control
Boost mode (chapter 5.9.1):
Setup Utility → AMD CBS → Zen Common Options → Core Performance Boost
- USB power
USB Power off in S5 (chapter 5.4.8):
Setup Utility → Advanced → NUCD Options → USB Power off in S5
or
USB VCC control (chapter 5.4.5):
Setup Utility → Advanced → USB Configuration

5.3 Main Menu

Once you enter the Setup Utility, the Main Menu will appear on the screen:



Fig. 65: Main Menu



This setup menu shows an overview of board configuration, CPU type, memory and firmware revisions.

BIOS

BIOS Settings	Options	Description
Language	<English>*	
System Time	No options	Set the time. Use Tab to switch between time elements [hour:min.:sec.]. Valid range is from 0 to 23, 0 to 59, 0 to 59. INCREASE/REDUCE: +/-
System Date	No options	Set the date. Use tab to switch between date elements [month/day/year]. Valid range is from 1 to 12, 1 to 31, 2000 to 2099. (Error checking will be done against month/day/year combinations that are not supported.) INCREASE/REDUCE: +/-
About this Software		

Tab. 14: Main Menu

5.4 Advanced Menu



Fig. 66: Advanced Menu

BIOS Settings	Options	Description
Boot Configuration	See submenu	Configures Boot Settings.
Peripheral Configuration	See submenu	Configures the peripheral devices.
IDE Configuration	See submenu	Select the IDE controller and hard disk drive type installed in your system
NVMe Configurations	See submenu	This function shows the connected NVMe devices.
USB Configuration	See submenu	Configure the USB support
ACPI Table/Features Control	See submenu	Configures ACPI Tables/Features setting.
CPU Related setting	See submenu	CPU Related setting
Above 4GB MMIO	<Disabled> <Enabled>*	Enable/Disable above 4GB MemoryMappedIO BIOS assignment. It's only available with Uefi Boot Mode.
NUCD Options	See submenu	Configure PIC watchdog, Wake On LAN, LVDS, Power LED Mode, KL 15 support, USB Power Off in S5

Tab. 15: Advanced Menu

BIOS

5.4.1 Boot Configuration

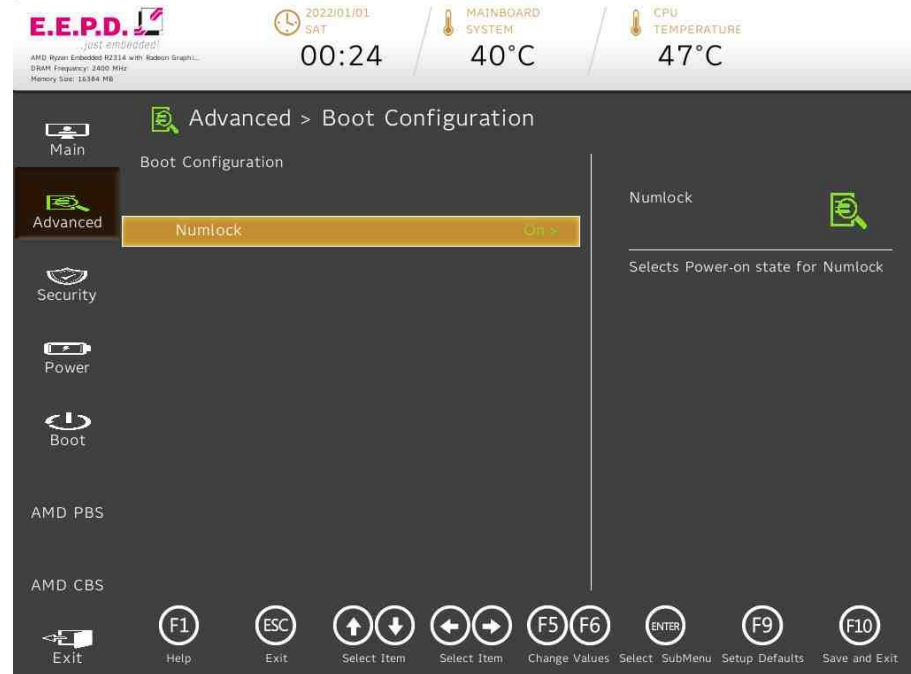


Fig. 67: Boot Configuration

BIOS Settings	Options	Description
Numlock	<Off> <On>*	Configuration of Numlock key at power up.

Tab. 16: Boot Configuration

5.4.2 Peripheral Configuration



Fig. 68: Peripheral Configuration

BIOS Settings	Options	Description
Trust Platform Module	<Disabled> <Enable discrete TPM>* <Enable firmware TPM>	Enable/Disable TPM physical presence. Need to reboot when set from disable to enable before selecting TPM Operation.
Erase fTPM NV for factory reset	<Disabled> <Enabled>*	Control if need to erase the TPM NV when fTPM factory reset flag set.

Tab. 17: Peripheral Configuration

BIOS

5.4.3 IDE Configuration



Fig. 69: IDE Configuration

BIOS Settings	Options	Description
SATA Configure as	<AHCI>*	Set SATA Configure Type
SATA Port 0	<Disabled> <Enabled>*	SATA Port 0 Enable/Disable
SATA Port 1	<Disabled> <Enabled>*	SATA Port 1 Enable/Disable
Serial ATA Port 0	See submenu	Serial ATA Port 0 Device configuration
Serial ATA Port 1	See submenu	Serial ATA Port 1 Device configuration
Serial ATA Port 2	See submenu	Serial ATA Port 2 Device configuration

Tab. 18: IDE Configuration

BIOS

5.4.4 NVMe Configurations

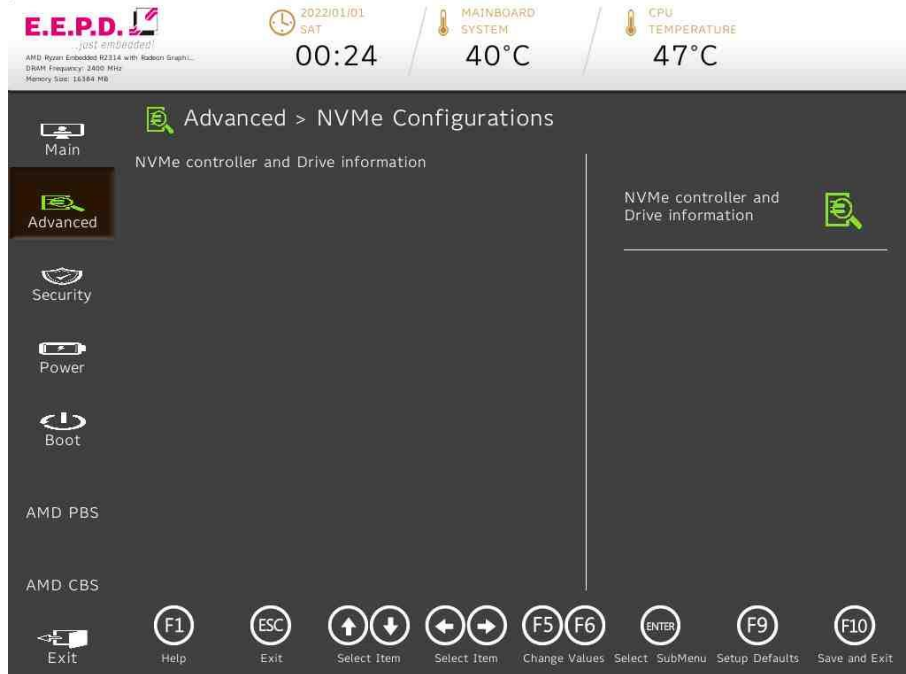


Fig. 70: NVMe Configuration

BIOS Settings	Options	Description
NVMe controller and drive information		This function shows the connected NVMe devices.

Tab. 19: NVMe Configurations

5.4.5 USB Configuration



Fig. 71: USB Configuration

BIOS Settings	Options	Description
Enable/Disable – VCC of USB Jacks	See submenu	Enable/Disable - USB VCC

Tab. 20: USB Configuration

5.4.5.1 Enable/Disable – VCC of USB Jacks



Fig. 72: Enable/Disable – VCC of USB Jacks

BIOS Settings	Options	Description
USB3.1 Rear Lower Port	<Disabled> <Enabled>*	This function allows you to enable or disable the power for the Rear Lower USB port.
USB3.1 Rear Upper Port	<Disabled> <Enabled>*	This function allows you to enable or disable the power for the Rear Upper USB port.
USB3.1 Front Port	<Disabled> <Enabled>*	This function allows you to enable or disable the power for the Front USB port.
USB3.1 Internal	<Disabled> <Enabled>*	This function allows you to enable or disable the power for the Internal USB port.

Tab. 21: Enable/Disable – VCC of USB Jacks



Note:

In order to not exclude yourself from the BIOS setup, at least one USB port should be enabled at all times. In case you select the “Disabled” option on all ports, the “USB3.1 Rear Lower” port will automatically be enabled by the system.

5.4.6 ACPI Table/Features Control



Fig. 73: ACPI Table/Features Control

BIOS Settings	Options	Description
HPET - HPET Support	<Disabled> <Enabled>*	High Precision Event Timer is supported in Windows Vista or above. HPET controller should not been seen in Windows XP no matter enable/disable in SCU. If this feature is enabled, the HPET table will be added into ACPI Tables.

Tab. 22: ACPI Table/Features Control

5.4.7 CPU Related Setting



Fig. 74: CPU Related Setting

BIOS Settings	Options	Description
SVM support	<Disabled> <Enabled>*	Enable/Disable SVM support. SVM mode is an option to enable a so-called secure virtual machine.

Tab. 23: CPU Related Setting

5.4.8 NUCD Options



Fig. 75: NUCD Options

Enabling watchdog leads to the reset of the board after time out. Please contact EEPD for further instructions.

CAUTION

BIOS Settings	Options	Description
PIC Watchdog	<Disabled>* <Enabled>	Enable/Disable the PIC watchdog
Watchdog Timeout (s)	Adjust value [30-254] Default value [40]*	Seconds before PIC watchdog times out. Range 30-254 seconds.
Wake On LAN	<Disabled> <Enabled>*	Enable/Disable wake on LAN
LVDS	<Disabled>* <Enabled>	Enable/Disable the eDP to LVDS transceiver
Power LED Mode	<Disabled> <Enabled>*	Set Power LED Mode (Enable/Disable)
KL 15 support (see also chapter 4.6)	<Disabled>* <Enabled>	KL 15 support: If enabled, set AMD CBS > FCH Common Options > AC Power Loss Options > Always Off To achieve minimum of power consumption when system is Off, set also in NUCD option > USB Power Off in S5 to Enabled (for all USB Ports) and Wake on LAN to Disabled In addition set in OS that power button press shuts down the system!
USB3.1 Rear Lower Port	<Disabled>* <Enabled>	Force USB VCC Off in S5. [Disabled]: Leaves VCC of USB Jack unchanged as in Advanced > USB Configuration > Enable/Disable - VCC of USB Jacks
USB3.1 Rear Upper Port	<Disabled>* <Enabled>	
USB3.1 Front Port	<Disabled>* <Enabled>	
USB3.1 Internal	<Disabled>* <Enabled>	
		[Enabled]: Switches USB VCC off in S5 (System Power Off)!

Tab. 24: NUCD Options

BIOS

5.5 Security Menu



Fig. 76: Security Menu

BIOS Settings	Options	Description
Current TPM Device	<Not Detected> <TPM 1.2> <TPM 2.0 (DTPM)>*	Current TPM Device: TPM1.2, or TPM2.0.
TrEE Protocol Version	<1.0> <1.1>*	TrEE Protocol Version: 1.0 or 1.1

TPM Availability	<Available>* <Hidden>	When hidden, don't expose TPM to OS
TPM Operation	<No Operation>* <Enable> <SetPCRbanks(Algorithm)> > <LogAllDigests> <SetPPRequiredForClear_Tru> <SetPPRequiredForClear_False> <SetPPRequiredForTurnOn_False> <SetPPRequiredForTurnOn_True> <SetPPRequiredForTurnOff_False> <SetPPRequiredForTurnOff_True> <SetPPRequiredForChangePCRs_False> <SetPPRequiredForChangePCRs_True> <SetPPRequiredForChangeEPS_False> <SetPPRequiredForChangeEPS_True> <ChangeEPS>	Select one of the supported operations to change TPM2 state.
Clear TPM	[] [X]	Clear TPM. Removes all TPM context associated with a specific Owner.
Set Supervisor Password	None	Install or change the password and the length of password must be greater than one character.

Tab. 25: Security Menu

BIOS

5.5.1 Storage Password Setup Page



Fig. 77: Storage Password Setup Page

BIOS Settings	Options	Description
TCG Storage Action	<No Operation>* <Enable_BlockSIDFunc> <Disable_BlockSIDFunc> <PPRequiredForEnableBlockSID_True> <PPRequiredForEnableBlockSID_False> <PPRequiredForDisableBlockSID_True> <PPRequiredForDisableBlockSID_False>	Change BlockSID actions, includes enable or disable BlockSID, Require or not require physical presence when remote enable or disable BlockSID

Tab. 26: Storage Password Setup Page

BIOS

5.6 Power Menu



Fig. 78: Power Menu

BIOS Settings	Options	Description
Wake on PME	<Disabled> <Enabled>*	Determines the action taken when the system power is off and a PCI Power Management Enable wake up event occurs.
Auto Wake on S5	<Disabled>* <By Every Day> <By Day of Month>	Auto wake on S5, By Day of Month or Fixed time of every day

Tab. 27: Power Menu

5.7 Boot Menu

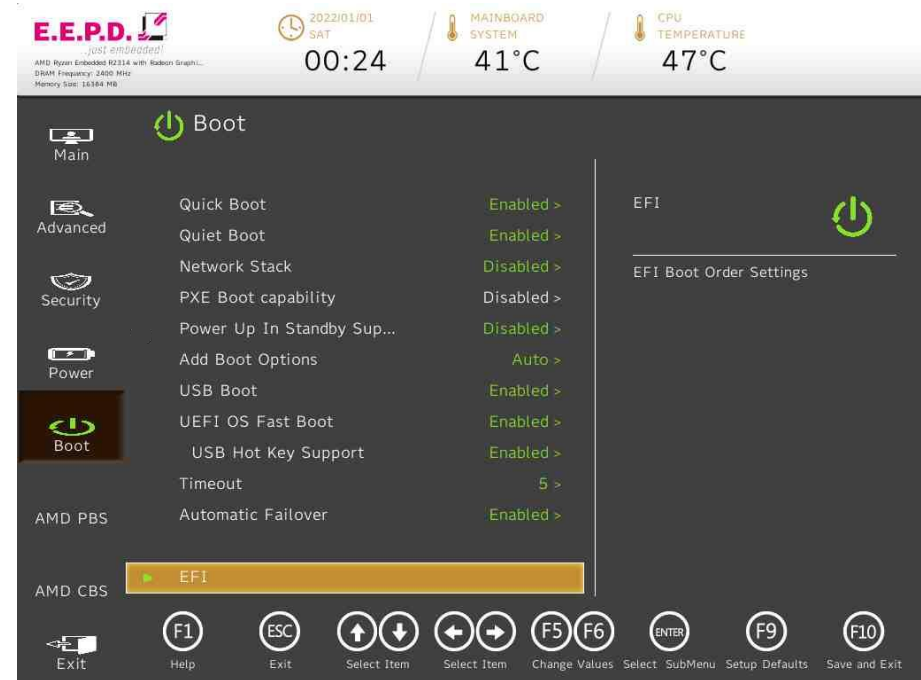


Fig. 79: Boot Menu

BIOS Settings	Options	Description
Quick Boot	<Enabled>* <Disabled>	Allows InsydeH2O to skip certain tests while booting. This will decrease the time needed to boot the system.
Quiet Boot	<Enabled>* <Disabled>	Disables or enables booting in Text Mode.
Network Stack	<Disabled>* <Enabled>	Network Stack Support: Windows 8 BitLocker Unlock UEFI IPv4/IPv6 PXE Legacy PXE OPROM

BIOS

PXE Boot capability	<Disabled>*	Disabled: Support Network Stack UEFI PXE: IPv4/IPv6 Legacy: Legacy PXE OPROM only
Power Up In Standby Support	<Enabled> <Disabled>*	Disable or enable Power Up In Standby Support. The PUIS feature set allows devices to be powered-up into the Standby power management state to minimize inrush current at power-up and to allow the host to sequence the spin-up of devices.
Add Boot Options	<First> <Last> <Auto>*	Position in Boot Order for Shell, Network and Removables
USB Boot	<Enabled>* <Disabled>	Disables or enables booting to USB boot devices.
UEFI OS Fast Boot	<Enabled>* <Disabled>	If enabled the system firmware does not initialize keyboard and check for firmware menu key.
USB Hot Key Support	<Disabled> <Enabled>*	Enable/Disable to support USB hot key while booting. This will decrease the time needed to boot the system.
Timeout	Adjust value [0-10] Default value [5]	The number of seconds that the firmware will wait before booting the original default boot selection.
Automatic Failover	<Disabled> <Enabled>*	Enable: if boot to default device fail, it will directly try to boot next device. Disable: if boot to default device fail, it will pop warning message then go into firmware UI.
EFI	See submenu	EFI Boot Order Settings

Tab. 28: Boot Menu

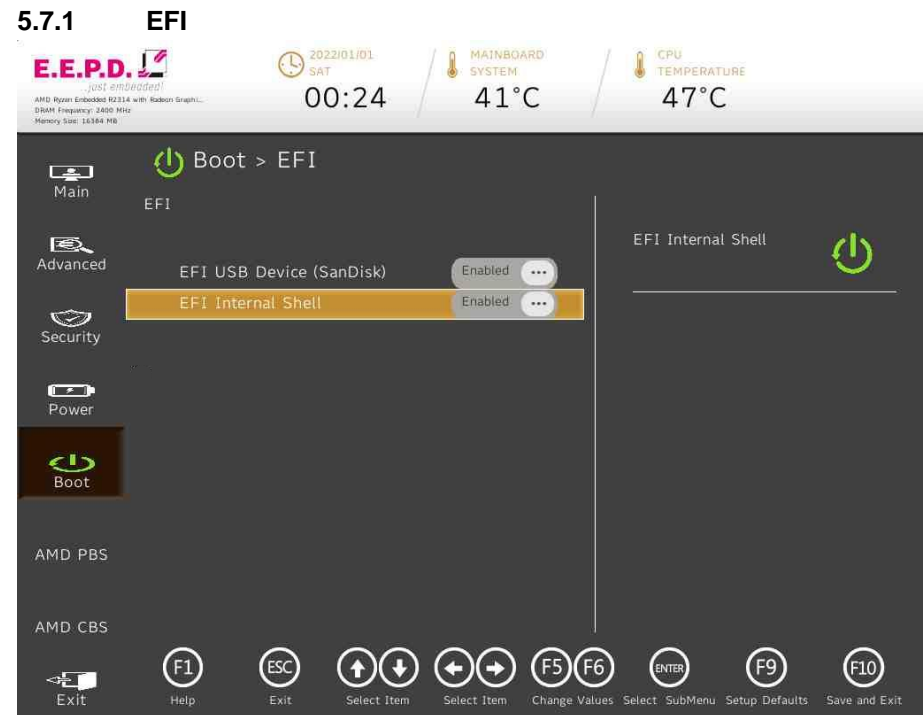


Fig. 80: EFI

BIOS Settings	Options	Description
EFI USB Device (SanDisk)	[]* [X]	
EFI Internal Shell	[]* [X]	

Tab. 29: EFI

BIOS

5.8 AMD PBS Option



Fig. 81: AMD PBS Option

BIOS Settings	Options	Description
AMD Firmware Version	Info page	Show all of AMD Firmware Version
M.2 Key B Power & Radio Control	<Power Disabled> <Power Only Enabled> <Power & Radio Operation Enabled>*	Enable/disable Power & Radio (WWAN) Operation of M.2 Key B Slot
M.2 Key E Radio Control	<Radio 1 & 2 (WLAN & BT) Operation Disabled> <Radio 1 (WLAN) Operation Enabled> <Radio 2 (BT) Operation Enabled> <Radio 1 & 2 (WLAN & BT) Operation Enabled>*	Enable/disable Radio operation of M.2 Key E Slot

Tab. 30: AMD PBS Option

5.8.1 AMD Firmware Version



Fig. 82: AMD Firmware Version

5.9 AMD CBS Option



Fig. 83: AMD CBS Option

BIOS Settings	Options	Description
Zen Common Options	See submenu	Zen Common Options
NBIO Common Options	See submenu	NBIO Common Options
FCH Common Options	See submenu	FCH Common Options

Tab. 31: AMD CBS Option

5.9.1 Zen Common Options



Fig. 84: Zen Common Options

BIOS Settings	Options	Description
Core Performance Boost	<Disabled>* <Auto>	This allows the processor to dynamically adjust and control the processor operating frequency to enable performance improvement.
CPU Thermal Throttling Control	<Disabled>* <Enabled>	CPU Thermal Throttling Enable/Disable

Tab. 32: Zen Common Options

5.9.2 NBIO Common Options



Fig. 85: NBIO Common Options

BIOS Settings	Options	Description
GFX Configuration	See submenu	GFX Configuration
CPU and Auxiliary Fan Control	See submenu	CPU and Auxiliary Fan Control
System Configuration	<12W – 35W POR Configuration> (depending on the System)	Warning: Select System Configuration may cause the system to hang, as some System Configuration may not be supported by your OPN.

Tab. 33: NBIO Common Options

5.9.2.1 GFX Configuration



Fig. 86: GFX Configuration

	<384M> <512M> <80M> <96M> <768M> <1G> <2G> <3G> <4G> <6G> <8G> <16G>	8GB of RAM or more, set the UMA buffer size to 1GB or 2GB
UMA Above 4G	<Disabled> <Enabled> <Auto>*	If requested UMA frame buffer size can't be fit under 4GB or the system has enough available memory above 4GB, this option may be set to TRUE to allow UMA frame buffer size to be allocated successfully.
NB Azalia	<Disabled> <Enabled> <Auto>*	Enable Integrate HD Audio controller

Tab. 34: GFX Configuration

BIOS

BIOS Settings	Options	Description
UMA Mode	<Auto> <UMA_SPECIFIED>* <UMA_AUTO>	UMA Mode Select UMA_SPECIFIED to set UMA Frame buffer size.
UMA Version	<Legacy> <Non-Legacy> <Hybrid Secure> <Auto>*	UMA Legacy Version UMA Non Legacy Version Hybrid Secure
UMA Frame buffer Size	<Auto>* <64M> <128M> <256M>	This allows the system to manage the amount of shared memory for graphics. For systems equipped with

5.9.2.2 CPU and Auxiliary Fan Control



Fig. 87: CPU and Auxiliary Fan Control

5.9.3 FCH Common Options



Fig. 88: FCH Common Options

BIOS Settings	Options	Description
CPU Fan Control	<Optimized Cooling>* <Silent Mode> <Maximum Cooling> <No Cooling>	Optimized Cooling → Automatic PWM control depending on temperature Silent Mode → Less noise (30%)
Auxiliary Fan Control	<Optimized Cooling> <Silent Mode> <Maximum Cooling> <No Cooling>*	Maximum Cooling → Always on (100%) No Cooling → Always off

Tab. 35: CPU and Auxiliary Fan Control

BIOS Settings	Options	Description
Ac Power Loss Options	See submenu	Ac Power Loss Options
UART Configuration Options	See submenu	Uart Configuration Options

Tab. 36: FCH Common Options

5.9.3.1 Ac Power Loss Options



Fig. 89: Ac Power Loss Options

BIOS Settings	Options	Description
Ac Loss Control	<Always Off> <Always On> <Reserved> <Previous>*	This function allows you to set the power status after a power failure. Select Always Off to keep the system power off after a power failure. Select Always On to turn the system power after a power failure. Select Previous to allow the System to resume its last power state before a power failure.

Tab. 37: Ac Power Loss Options

5.9.3.2 UART Configuration Options



Fig. 90: UART Configuration Options

BIOS Settings	Options	Description
Uart 0 Enable	<Disabled> <Enabled>*	Enable or disable the UART0 port.
Uart 0 Legacy Options	<Disabled> <COM1 0x3F8>* <COM2 0x2F8> <COM3 0x3E8> <COM4 0x2E8>	This function specifies the base I/O port address of a user-specified serial port.
Uart 0 Mode	<RS232>* <RS485 Half Duplex> <RS485/RS422 Full Duplex>	Select UART 0 operating mode
Uart 0 Termination	<120 Ohm diff.> <Termination Off>*	Select UART 0 transceiver termination
Uart 1 Enable	<Disabled> <Enabled>*	This function allows you to enable or disable the UART1 port.
Uart 1 Legacy Options	<Disabled> <COM1 0x3F8> <COM2 0x2F8>* <COM3 0x3E8> <COM4 0x2E8>	This function specifies the base I/O port address of a user-specified serial port.

Tab. 38: UART Configuration Options

5.10 Exit Menu



Fig. 91: Exit Menu

BIOS Settings	Options	Description
Exit Saving Changes		Exit system setup after saving your changes.
Save Change Without Exit		Save your changes without exiting system.
Exit Discarding Changes		Exit system setup without saving your changes.
Load Optimal Defaults		Load Optimal Defaults to all the setup options.
Load Custom Defaults		Load Custom Defaults to all the setup options.
Save Custom Defaults		Save changes done so far as Custom Defaults.
Discard Changes		Discard Changes done so far to any of the setup options.

Tab. 39: Exit Menu

Index of Figures

Fig. 1: System overview 11
 Fig. 2: Top view 12
 Fig. 3: Bottom view 12
 Fig. 4: Front view 13
 Fig. 5: recommended cutouts front panel, values [mm] 13
 Fig. 6: Rear view 13
 Fig. 7: recommended cutouts rear panel, values [mm] 13
 Fig. 8: Side view 13
 Fig. 9: Dimensions bottom side, values [mm]..... 14
 Fig. 10: Dimensions top side, values [mm]..... 14
 Fig. 11: Dimensions front side, values [mm]..... 15
 Fig. 12: Dimensions side, values [mm]..... 15
 Fig. 13: Dimensions threaded bolts, values [mm] 15
 Fig. 14: Connection overview top side..... 17
 Fig. 15: Connection overview bottom side..... 18
 Fig. 16: Mini-DP++ connector detail 19
 Fig. 17: Mini-DP++ connector schematic..... 19
 Fig. 18: Dual-USB 3.2 port detail..... 20
 Fig. 19: Dual-USB 3.2 port schematic 20
 Fig. 20: USB 3.2 port detail 20
 Fig. 21: USB 3.2 port schematic..... 20
 Fig. 22: Ethernet dual-port detail 21
 Fig. 23: Ethernet port schematic..... 21
 Fig. 24: Molex RS232 connector detail..... 21
 Fig. 25: Molex RS232 connector schematic 21
 Fig. 26: Molex RS232/485 connector detail..... 22
 Fig. 27: Molex RS232/485 connector schematic..... 22
 Fig. 28: Power connector detail 23
 Fig. 29: Power connector schematic 23
 Fig. 30: DDR4 SO-DIMM socket detail..... 24

Fig. 31: RAM assembly 24
 Fig. 32: Molex audio connector detail 25
 Fig. 33: Molex audio connector schematic 25
 Fig. 34: M.2 Key M connector detail 26
 Fig. 35: M.2 Key E connector detail..... 26
 Fig. 36: M.2 Key B connector detail..... 26
 Fig. 37: M.2 Key B module assembly 26
 Fig. 38: SATA data connector detail 27
 Fig. 39: SATA data connector schematic 27
 Fig. 40: Molex SATA power connector detail 27
 Fig. 41: Molex SATA power connector schematic..... 27
 Fig. 42: LVDS data connector detail 28
 Fig. 43: LVDS dataconnector schematic 28
 Fig. 44: STL3X1 detail 28
 Fig. 45: STL3X1 schematic..... 28
 Fig. 46: Molex LC-display backlight connector detail 29
 Fig. 47: Molex LC-display backlight connector detail 29
 Fig. 48: Molex feature connector detail..... 30
 Fig. 49: Molex feature connector schematic..... 30
 Fig. 50: Molex battery connector detail..... 31
 Fig. 51: Molex battery connector schematic 31
 Fig. 52: Battery detail 31
 Fig. 53: Molex power button connector detail 32
 Fig. 54: Molex power button connector schematic 32
 Fig. 55: Power button/power and SATA LEDs detail..... 32
 Fig. 56: Molex reset button connector detail..... 33
 Fig. 57: Molex reset button connector schematic 33
 Fig. 58: MicroSD card slot detail 33
 Fig. 59: MicroSIM card slot detail 33
 Fig. 60: Molex CPU fan connector detail 34
 Fig. 61: Molex auxiliary fan connector detail..... 34
 Fig. 62: Molex fan connectors schematic 34
 Fig. 63: Molex USB internal connector detail 35

Fig. 64: Molex USB internal connector schematic..... 35
 Fig. 65: Main Menu 37
 Fig. 66: Advanced Menu..... 38
 Fig. 67: Boot Configuration 39
 Fig. 68: Peripheral Configuration 39
 Fig. 69: IDE Configuration 40
 Fig. 70: NVMe Configuration 41
 Fig. 71: USB Configuration 41
 Fig. 72: Enable/Disable – VCC of USB Jacks 42
 Fig. 73: ACPI Table/Features Control 43
 Fig. 74: CPU Related Setting 43
 Fig. 75: NUCD Options 44
 Fig. 76: Security Menu..... 45
 Fig. 77: Storage Password Setup Page 46
 Fig. 78: Power Menu..... 47
 Fig. 79: Boot Menu 47
 Fig. 80: EFI 48
 Fig. 81: AMD PBS Option 49
 Fig. 82: AMD Firmware Version 50
 Fig. 83: AMD CBS Option..... 50
 Fig. 84: Zen Common Options 51
 Fig. 85: NBIO Common Options 51
 Fig. 86: GFX Configuration 52
 Fig. 87: CPU and Auxiliary Fan Control..... 53
 Fig. 88: FCH Common Options 53
 Fig. 89: Ac Power Loss Options 54
 Fig. 90: UART Configuration Options 55
 Fig. 91: Exit Menu 56

Index of Tables

Tab. 1: RS232 connector.....21
 Tab. 2: RS232/485 connector.....22
 Tab. 3: Power connector.....23
 Tab. 4: Audio connector.....25
 Tab. 5: SATA power connector27
 Tab. 6: STL3X1.....28
 Tab. 7: LC-display backlight connector29
 Tab. 8: Feature connector30
 Tab. 9: Battery connector31
 Tab. 10: Power button connector32
 Tab. 11: Reset button connector33
 Tab. 12: Fan connectors.....34
 Tab. 13: USB internal connector35
 Tab. 14: Main Menu.....37
 Tab. 15: Advanced Menu.....38
 Tab. 16: Boot Configuration.....39
 Tab. 17: Peripheral Configuration.....39
 Tab. 18: IDE Configuration40
 Tab. 19: NVMe Configurations41
 Tab. 20: USB Configuration.....41
 Tab. 21: Enable/Disable – VCC of USB Jacks.....42
 Tab. 22: ACPI Table/Features Control43
 Tab. 23: CPU Related Setting43
 Tab. 24: NUCD Options.....44
 Tab. 25: Security Menu.....45
 Tab. 26: Storage Password Setup Page46
 Tab. 27: Power Menu47
 Tab. 28: Boot Menu48
 Tab. 29: EFI48
 Tab. 30: AMD PBS Option.....49

Tab. 31: AMD CBS Option.....50
 Tab. 32: Zen Common Options51
 Tab. 33: NBIO Common Options.....51
 Tab. 34: GFX Configuration52
 Tab. 35: CPU and Auxiliary Fan Control.....53
 Tab. 36: FCH Common Options53
 Tab. 37: Ac Power Loss Options54
 Tab. 38: UART Configuration Options55
 Tab. 39: Exit Menu.....56

Terminology

BT	Bluetooth
CPU	Central Processing Unit or processor
DC	Direct Current
DDR4	Fourth generation „Double Data Rate“ memory technology
DP	Display port
EMI	ElectroMagnetic Interference
2.5 Gigabit Ethernet	Ethernet connection with a frame transfer speed up to 2500 Mbit/s
GND	Ground
MIC	Microphone
M.2	Next generation mSATA
OCP	Over Current Protection
PWM	Pulse-Width Modulation
RAM	Random Access Memory
RS-232	Serial standard interface
RS-485	Serial standard interface
SD	Secure Digital memory card
SIM	Subscriber Identity Module (Card) used to store information in mobile phones
SoC	System on a Chip means the integration of all or a large part of the functions of a programmable electronic system on a single chip
SODIMM	Small Outline Dual Inline Memory Module
SSD	Solid State Drive
USB	Universal Serial Bus
Watchdog	A watchdog (timer) is a computer hardware timing device that triggers a system reset if the main program hangs, in order to keep the computer running
WLAN	Wireless Local Area Network
WWAN	Wireless Wide Area Network

Classified Index

-A-		
Advanced Menu	38	
AMD CBS Option	50	
AMD PBS Option	49	
Assembly	12	
Audio Connector	25	
-B-		
Battery	31	
Battery Connector	31	
BIOS	36	
Boot Menu	47	
-C-		
Connection Overview	17	
-D-		
DDR4 SO-DIMM Sockets	24	
Dimensions	14	
Dual-USB 3.2 Port	20	
-E-		
Environmental Specification	10	
Exit Menu	56	
-F-		
FAN Connectors	34	
Feature Connector	30	
Feature Details	16	
Feature Overview	8	
-G-		
Gigabit Ethernet Dual-Port	21	
-L-		
LC-Display Backlight Connector	29	
LVDS Connector	28	
-M-		
M.2 Sockets	26	
Main Menu	37	
MicroSD Card Slot	33	
MicroSIM Card Slot	33	
Mini-DP++ Connectors	19	
Most Common Settings	36	
-P-		
Power Button Connector	32	
Power Button, Power and SATA LEDs	32	
Power Connector	23	
Power Menu	47	
Product Views	12	
-R-		
Rear USB 3.2 Port	20	
Reset Button Connector	33	
RS232 Connector	21	
RS232/485 Connector	22	
-S-		
SATA Data Connector	27	
SATA Power Connector	27	
Security Menu	45	
System Overview	11	
-U-		
USB Internal Connector	35	

